

Single 2-Input NAND Gate

General Description

The ET74AHC1G00 is a single 2-input NAND Gate operating from a 2V to 5.5V supply. This device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive.

Features

- Designed for 2V to 5.5V V_{CC} Operation
- Over-voltage Tolerant Inputs Accept Voltages to 5.5V
- ±8mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- These Devices are Pb-Free and RoHS Compliant
- ESD Protection Complies with JESD22 Standard
 - HBM: ±4000V Pass (JEDEC JS-001)
 - CDM: ±1000V Pass (JEDEC JS-002)
- Latch-up Performance Exceeds ±100mA per JEDEC JESD78F
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74AHC1G00	SC70-5 (1.3mm × 2.1mm)	Tape and Reel, 3K/Reel	1
ET74AHC1G00T	SOT23-5 (1.6mm × 2.9mm)	Tape and Reel, 3K/Reel	3
ET74AHC1G00Y	DFN6 (1.0mm × 1.5mm)	Tape and Reel, 3K/Reel	1

Pin Configuration

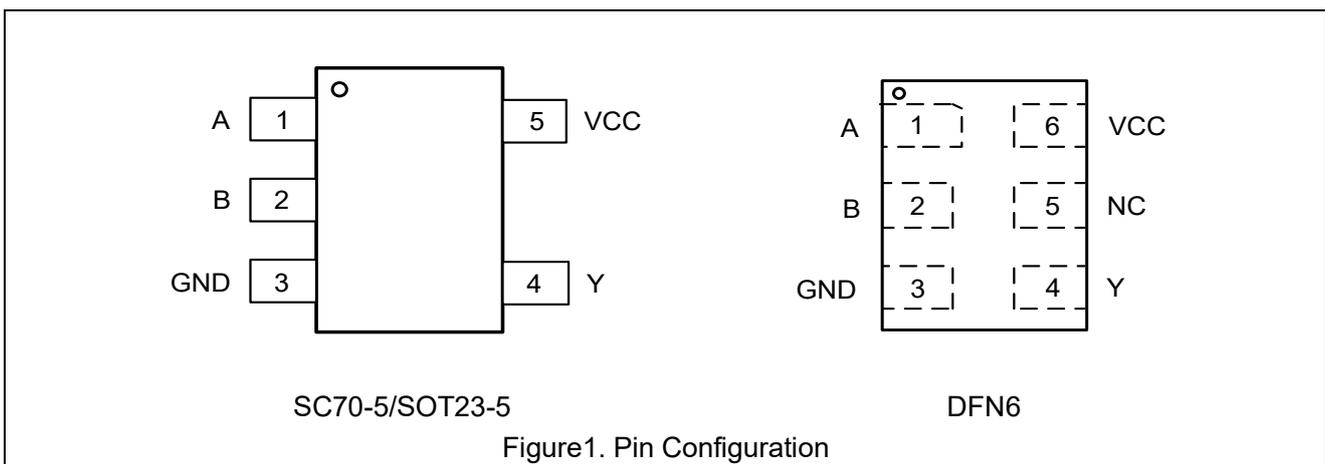


Figure1. Pin Configuration

ET74AHC1G00

Pin Function

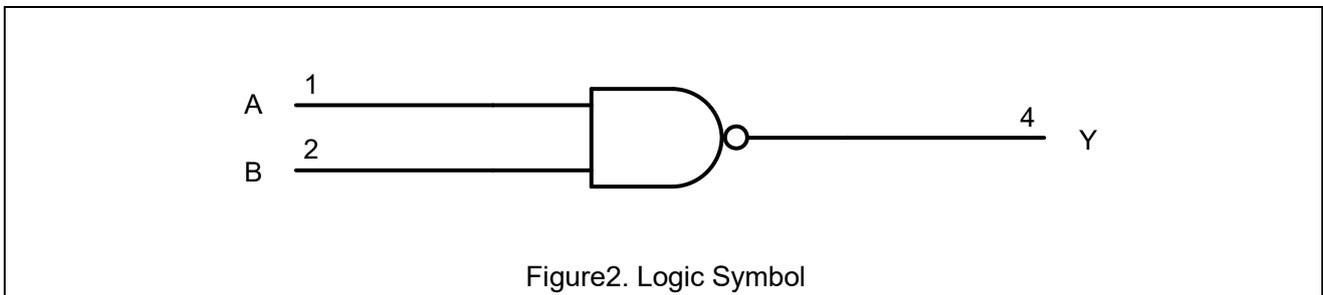
SC70-5 / SOT23-5

Pin No.	Pin Name	Function
1	A	Input A
2	B	Input B
3	GND	Ground
4	Y	Output Y
5	VCC	Supply Voltage

DFN6

Pin No.	Pin Name	Function
1	A	Input A
2	B	Input B
3	GND	Ground
4	Y	Output Y
5	NC	No Connect
6	VCC	Supply Voltage

Block Diagram



Function Table

Input		Output
A	B	Y
L	L	H
L	H	H
H	L	H
H	H	L

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Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V _{CC}	DC Supply Voltage (VCC Pin)		-0.5 to 7.0	V
V _I	DC Input Voltage ⁽¹⁾		-0.5 ≤ V _I ≤ 7.0	V
V _O	DC Output Voltage Output in Higher or Low State		-0.5 to V _{CC} + 0.5	V
I _{IK}	DC Input Diode Current, V _I < GND		-20	mA
I _{OK}	DC Output Diode Current, V _O < GND, V _O > V _{CC}		±20	mA
I _O	DC Output Sink Current		±25	mA
I _{CC}	DC Supply Current Per Supply Pin		75	mA
I _{GND}	DC Ground Current Per Supply Pin		-75	mA
T _{STG}	Storage Temperature Range		-65 to 150	°C
T _L	Lead Temperature, Soldering 10 Seconds		260	°C
T _J	Max Junction Temperature		150	°C
V _{ESD}	ESD Classification	Human Body Model ⁽²⁾	±4000	V
		Charged Device Model ⁽³⁾	±1000	
I _{LU}	Max Latch Up Current Above V _{CC} and GND at 125°C ⁽⁴⁾		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: I_O absolute maximum rating must be observed.

Note2: HBM tested per JEDEC JS-001;

Note3: CDM tested per JEDEC JS-002;

Note4: Latch Up Current Maximum Rating tested per JEDEC JESD78F.

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
R _{θJA}	SC70-5	Thermal Characteristics, Thermal Resistance, Junction-to-Air	300	°C/W
	SOT23-5		250	
	DFN6		440	
P _D	SC70-5	Power Dissipation in Still Air at 85°C	215	mW
	SOT23-5		260	
	DFN6		150	

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Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{CC}	DC Supply Voltage Operating	2	5.5	V
V_I	DC Input Voltage	0	V_{CC}	V
V_O	DC Output Voltage (High or Low State)	0	V_{CC}	V
T_A	Operating Temperature Range	-40	125	°C

Electrical Characteristics

DC Electrical Characteristics

Symbol	Parameter	Condition	$V_{CC}(V)$	$T_A = 25^\circ C$			$-40^\circ C \leq T_A \leq 125^\circ C$		Unit
				Min	Typ	Max	Min	Max	
V_{IH}	High-Level Input Voltage		2.0	1.5			1.5		V
			3.0	2.1			2.1		
			5.5	3.85			3.85		
V_{IL}	Low-Level Input Voltage		2.0			0.5		0.5	V
			3.0			0.9		0.9	
			5.5			1.65		1.65	
V_{OH}	High-Level Output Voltage	$I_{OH} = -50\mu A$	2~5.5	$V_{CC} - 0.1$	V_{CC}		$V_{CC} - 0.1$		V
		$I_{OH} = -4mA$	3	2.58			2.4		
		$I_{OH} = -8mA$	4.5	3.54			3.7		
V_{OL}	Low-Level Output Voltage	$I_{OL} = 50\mu A$	2~5.5			0.1		0.1	V
		$I_{OL} = 4mA$	3			0.36		0.55	
		$I_{OL} = 8mA$	4.5			0.36		0.55	
I_{IN}	Input Leakage Current	$V_I = 5.5V$ or GND	0~5.5			0.1		2.0	μA
I_{OFF}	Power Off Leakage Current	$V_I = 5.5V$ or $V_O = 5.5V$	0			1		10	μA
I_{CC}	Quiescent Supply Current	$V_I = 5.5V$ or GND	5.5			1		40	μA

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AC Electrical Characteristics

$t_r = t_f = 3\text{ns}$;

Symbol	Parameter	Condition	V _{CC} (V)	T _A = 25°C			-40°C ≤ T _A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
t _{PLH} t _{PHL}	Propagation Delay (Figure3 and 4)	C _L = 15pF	3~3.6		7.5	11	1	13.5	ns
		C _L = 50pF	3~3.6		9.5	16	1	19	
		C _L = 15pF	4.5~5.5		6.4	8.4	1	9.9	
		C _L = 50pF	4.5~5.5		7.9	10.4	1	12.4	

Capacitance Characteristics

Symbol	Parameter	Condition	Typ	Unit
C _{IN}	Input Capacitance	V _{CC} = 5.5V, V _I = 0V or V _{CC}	5	pF
C _{PD}	Power Dissipation Capacitance ⁽⁵⁾	10MHz, V _{CC} = 3.3V, V _I = 0V or V _{CC}	21	pF
		10MHz, V _{CC} = 5.5V, V _I = 0V or V _{CC}	21	

Note5. C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \sum(C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i = input frequency in MHz;

f_o = output frequency in MHz;

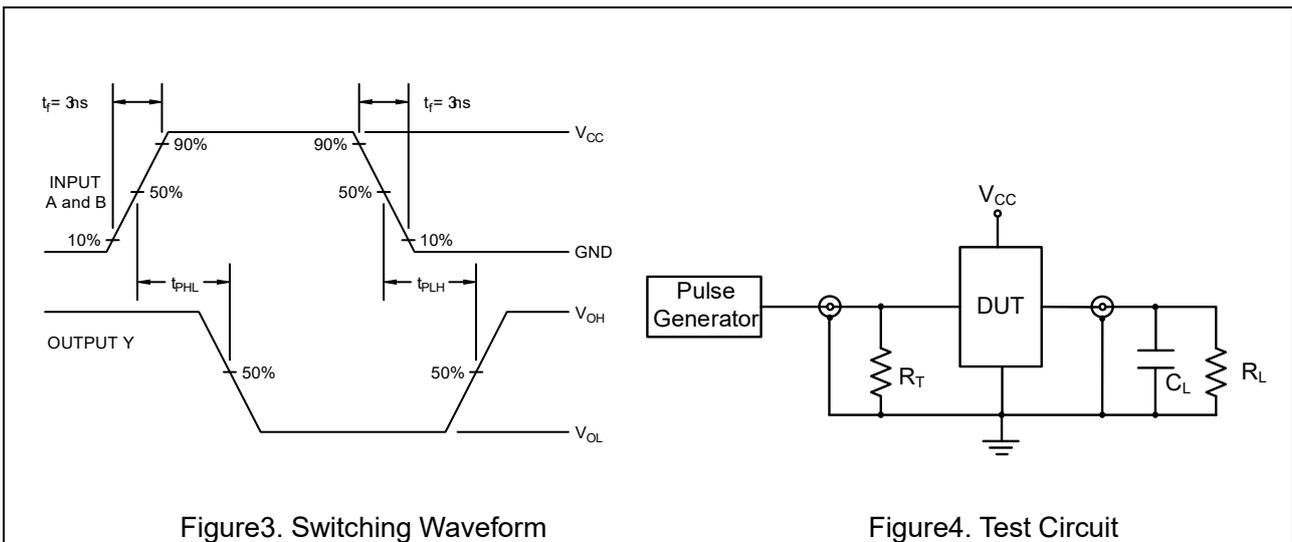
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\sum(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

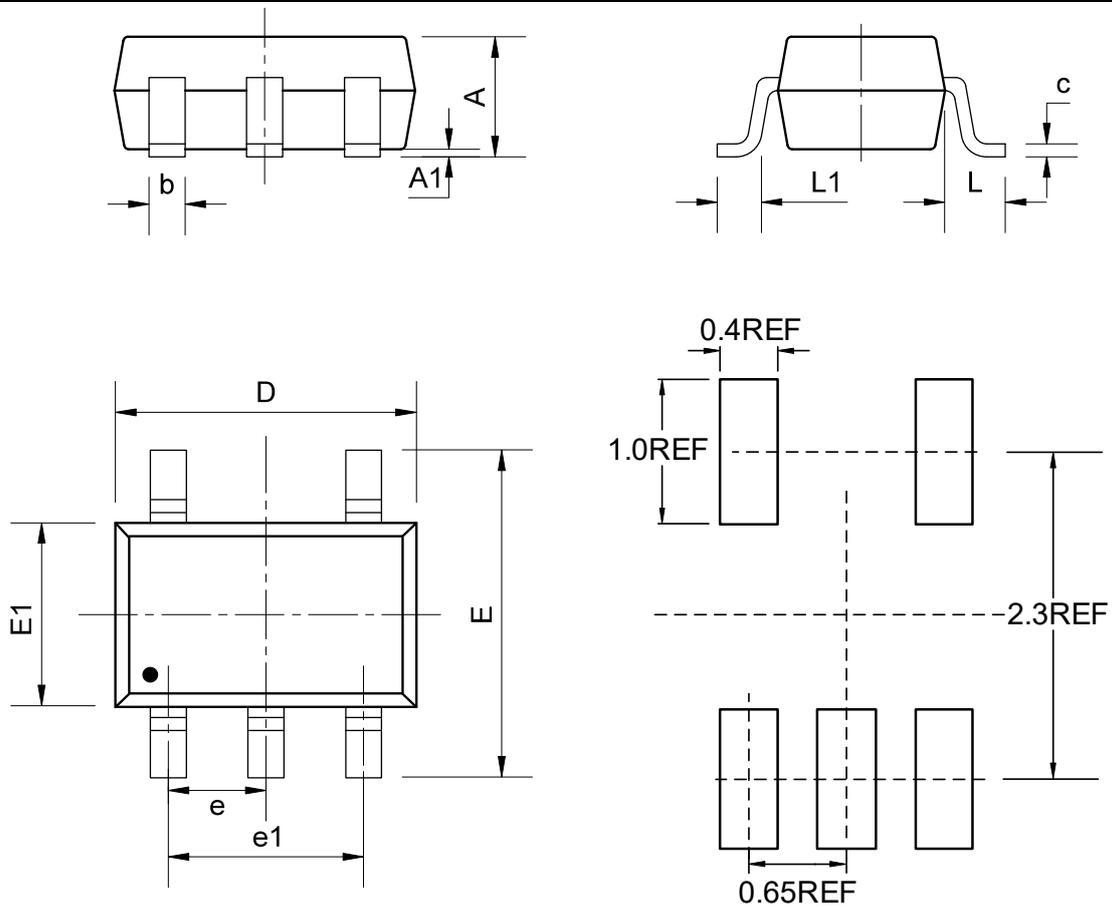
AC Test Circuit



ET74AHC1G00

Package Dimension

SC70-5 (1.3mm × 2.1mm)



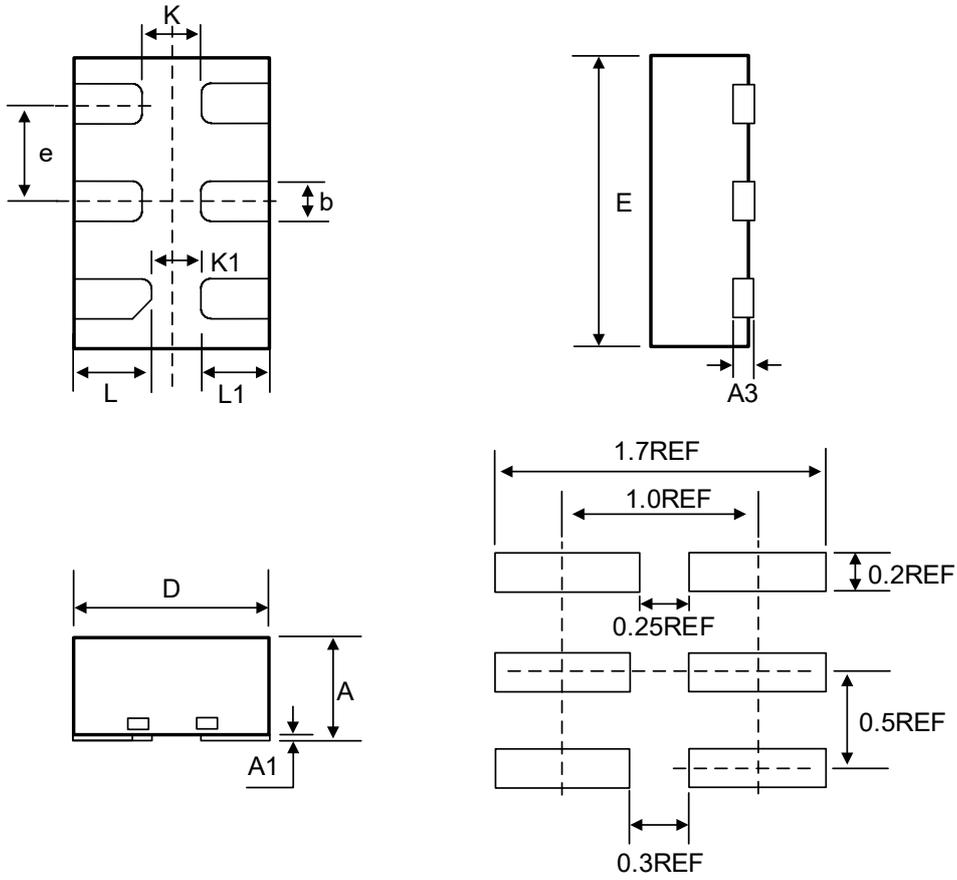
COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.10
A1	0.00	-	0.15
b	0.15	-	0.35
c	0.08	-	0.20
D	2.00	2.10	2.30
e	0.65BSC		
e1	1.30BSC		
E	2.15	2.30	2.50
E1	1.15	1.30	1.45
L	0.50REF		
L1	0.33REF		

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DFN6 (1.0mm × 1.5mm)



COMMON DIMENSIONS

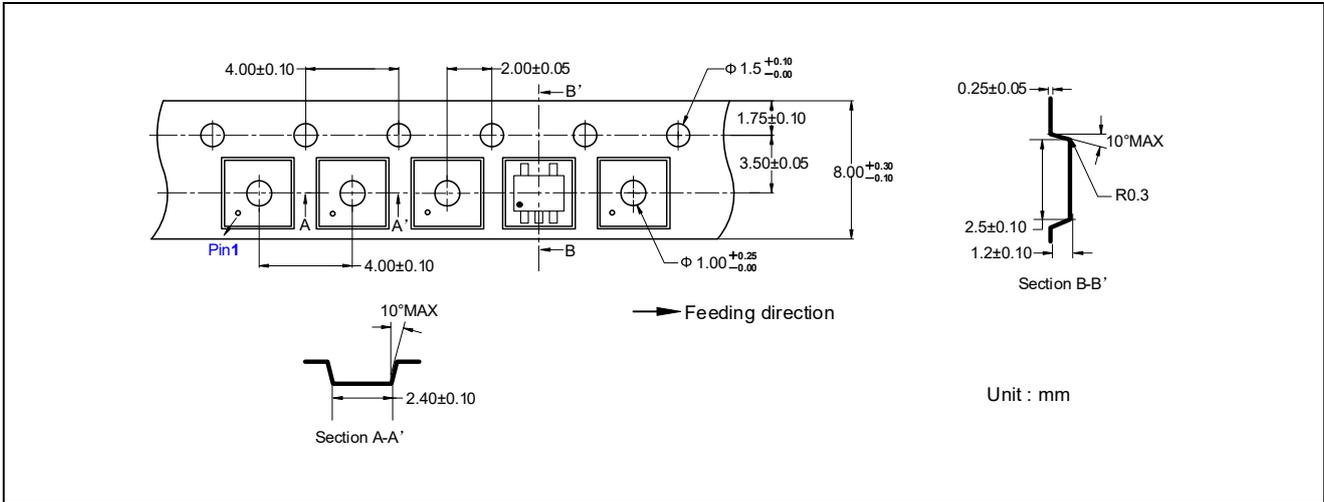
(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.50	--	0.60
A1	0.00	0.02	0.05
A3	0.10REF		
b	0.15	0.20	0.25
D	0.90	1.00	1.10
E	1.40	1.50	1.60
e	0.50BSC		
K	0.30REF		
K1	0.25REF		
L	0.35	0.40	0.45
L1	0.30	0.35	0.40

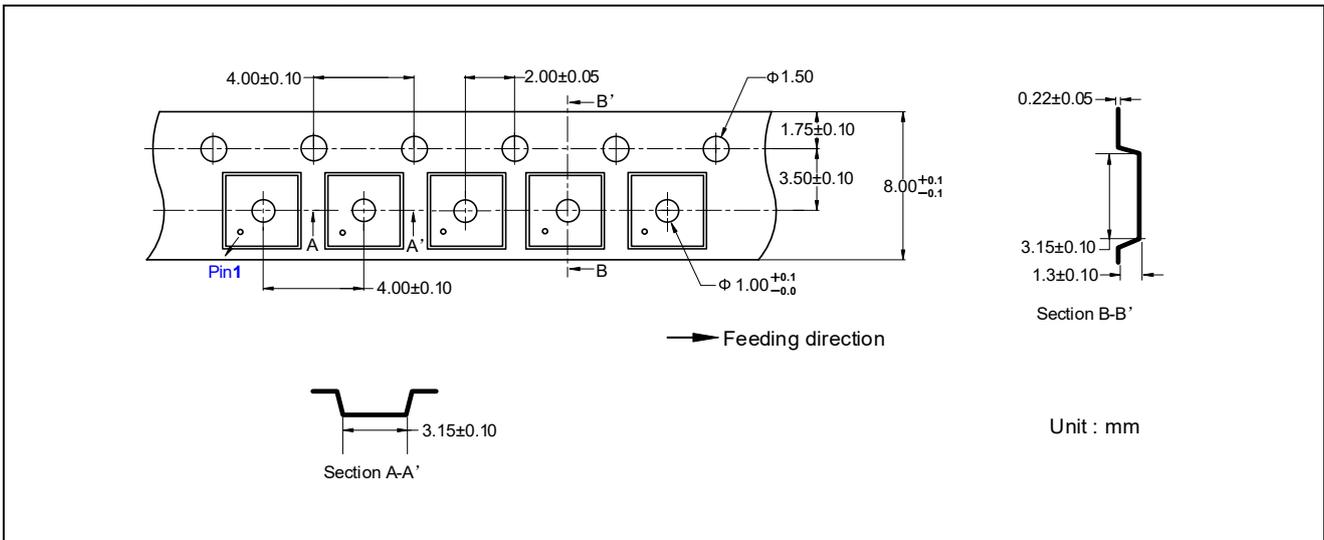
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Tape Information

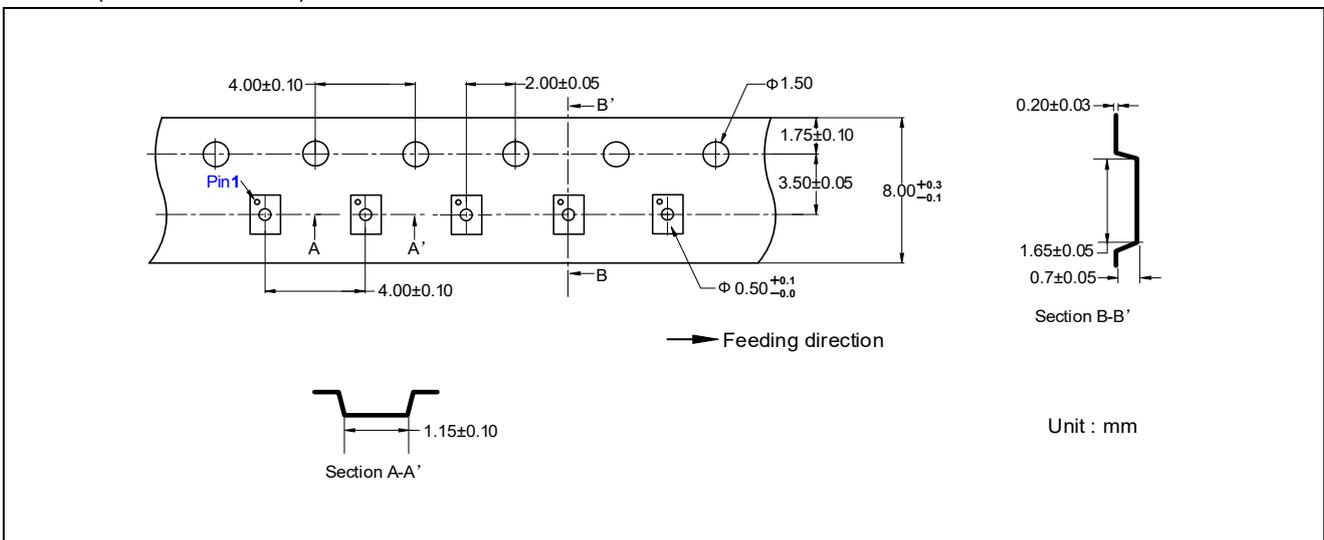
SC70-5 (1.3mm × 2.1mm)



SOT23-5 (1.6mm × 2.9mm)

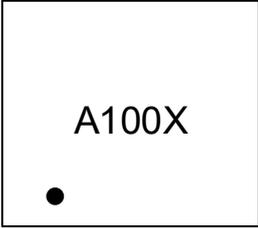
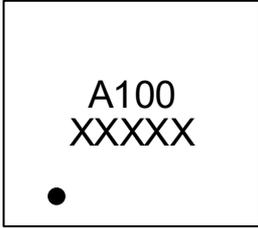


DFN6 (1.0mm × 1.5mm)



ET74AHC1G00

Marking Information

	
ET74AHC1G00 A100 = Part Number X = Tracking Number	ET74AHC1G00T A100 = Part Number XXXXX = Tracking Number

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2025-08-28	Original Version	Wang anran	Yang xiaoxu	Liu jiaying