Single Non−Inverting Buffer with Open Drain Output

General Description

The ET74HC1G07 is a high performance single non−inverting buffer with open drain outputs operating from a 2V to 6V supply. The Output stage is open drain with Over Voltage Tolerance.

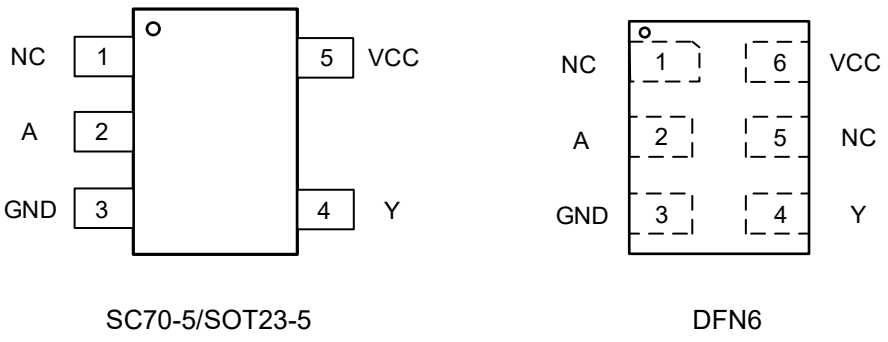
This allows the ET74HC1G07 to be used to interface 5.0V circuits to circuits of any voltage between 0V and +7.0V.

Features

- Designed for 2V to 6V V_{CC} Operation
- Over-voltage Tolerant Inputs
- 2.6mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- These Devices are Pb−Free and are RoHS Compliant
- ESD Protection Complies with JESD22 Standard
  -- HBM: ±4000V Pass (JEDEC JS-001)
  -- CDM: ±1000V Pass (JEDEC JS-002)
  Latch-up Performance Exceeds ±100mA per JEDEC JESD78F
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74HC1G07	SC70-5 (1.3mm × 2.1mm)	Tape and Reel, 3K/Reel	Level 1
ET74HC1G07T	SOT23-5 (1.6mm × 2.9mm)	Tape and Reel, 3K/Reel	Level 3
ET74HC1G07Y	DFN6 (1.0mm × 1.5mm)	Tape and Reel, 3K/Reel	Level 1

Pin Configuration



SC70-5/SOT23-5 DFN6

TOP VIEW

Figure1. Pin Configuration

Pin Function

(SC70-5/ SOT23-5)

Pin No.	Pin Name	Function
1	NC	No Connect
2	A	Input
3	GND	Ground
4	Y	Output
5	VCC	Supply Voltage

DFN6

Pin No.	Pin Name	Function
1	NC	No Connect
2	A	Input
3	GND	Ground
4	Y	Output
5	NC	No Connect
6	VCC	Supply Voltage

Block Diagram

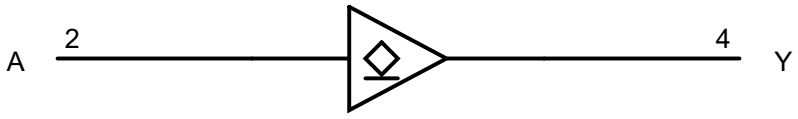


Figure2. Logic Symbol

Function Table

Input A	Output Y
L	L
H	Z

Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V_{CC}	DC Supply Voltage(VCC Pin)		-0.5 to 7.0	V
V_I	DC Input Voltage[1]		-0.5 ≤ V_I ≤ +7.0	V
V_O	DC Output Voltage Output in Higher or Low State		-0.5 to V_{CC} + 0.5	V
I_{IK}	DC Input Diode Current V_I < GND		−50	mA
I_{OK}	DC Output Diode Current V_O < GND, V_O > V_{CC}		±50	mA
I_O	DC Output Sink Current		±50	mA
I_{CC}	DC Supply Current Per Supply Pin		+100	mA
I_{GND}	DC Ground Current Per Supply Pin		−100	mA
R_{IN}	A & V_{CC} input impedance		≥50	MΩ
R_{OUT}	OUT output impedance at High-Impedance state		≥50	MΩ
	OUT output impedance at Low state		100	Ω
T_{STG}	Storage Temperature Range		-65 to 150	°C
T_L	Lead Temperature, Soldering 10 Seconds		260	°C
T_J	Max Junction Temperature		150	°C
V_{ESD}	ESD Classification	Human Body Model [2]	±4000	V
		Charged Device Model [3]	±1000	
I_{LU}	Max Latch Up Current Above V_{CC} and GND at 125°C [4]		±100	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1. I_O absolute maximum rating must be observed.

Note2: HBM tested per JEDEC JS-001;

Note3: CDM tested per JEDEC JS-002;

Note4: Latch Up Current Maximum Rating tested per JEDEC JESD78F.

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
$R_{\theta JA}$	SC70-5	Thermal Characteristics, Thermal Resistance, Junction-to-Air	300	°C/W
	SOT23-5		250	
	DFN6		440	
P_D	SC70-5	Power Dissipation in Still Air at 85°C	215	mW
	SOT23-5		260	
	DFN6		150	

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V_{CC}	DC Supply Voltage	2	6	V
V_{IN}	DC Input Voltage	0	6	V
V_{OUT}	DC Output Voltage(High or Low State)	0	6	V
T_A	Operating Temperature Range	-40	125	°C

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied.

Electrical Characteristics

DC Electrical Characteristics

Symbol	Parameter	Condition	V_{CC}(V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
V_{IH}	High−Level Input Voltage		2.0	1.5			1.5		V
			4.5	3.15			3.15		
			6.0	4.2			4.2		
V_{IL}	Low−Level Input Voltage		2.0			0.5		0.5	V
			4.5			1.35		1.35	
			6.0			1.8		1.8	
V_{OL}	Low−Level Output Voltage	I_{OL} = 20uA	2~6		0.0	0.1		0.1	V
		I_{OL} = 2mA	4.5		0.15	0.33		0.4	
		I_{OL} = 2.6mA	6.0		0.16	0.33		0.4	
I_{IN}	Input Leakage Current	V_{IN} = 6V or GND	0~6		±0.1			±1.0	μA
I_{OFF}	Power Off Leakage Current	V_{IN} = 6V or V_{OUT} = 6V	0			1		10	μA
I_{CC}	Quiescent Supply Current	V_{IN} = 6V or GND	6			1		10	μA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

AC Electrical Characteristics

$t_r = t_f = 2.5ns$, $R_1 = R_L = 1K\Omega$;

Symbol	Parameter	Condition	V_{CC}(V)	T_A = 25 °C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
t_{PLZ} t_{PZL}	Propagation Delay (Figure3 and 4)	C_L= 50pF	2.0		31	121		141	ns
		C_L= 50pF	4.5		11	25		29	
		C_L= 15pF	5.0		8				
		C_L= 50pF	6.0		8.5	20.5		23.5	

Capacitance Characteristics

Symbol	Parameter	Condition	Typ	Unit
C_{IN}	Input Capacitance	V_{CC} = 6V, V_I = 0V or V_{CC}	5	pF
C_{PD}	Power Dissipation Capacitance(5)	10MHz, V_{CC} = 3.3V, V_I = 0V or V_{CC}	26	pF
		10MHz, V_{CC}= 6V, V_I = 0V or V_{CC}	30	

Note5. C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

AC Test Circuit

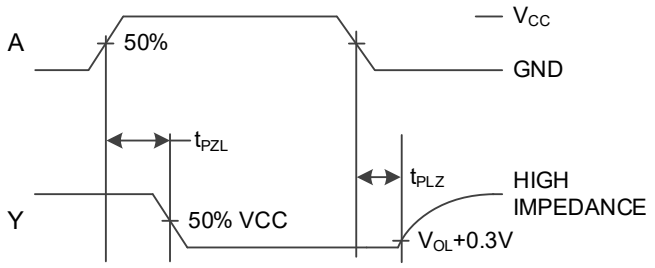


Figure3. Switching Waveforms

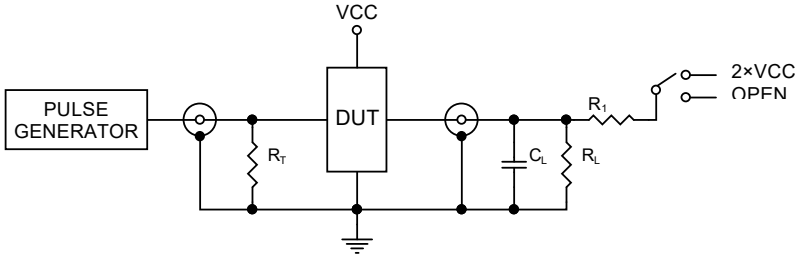
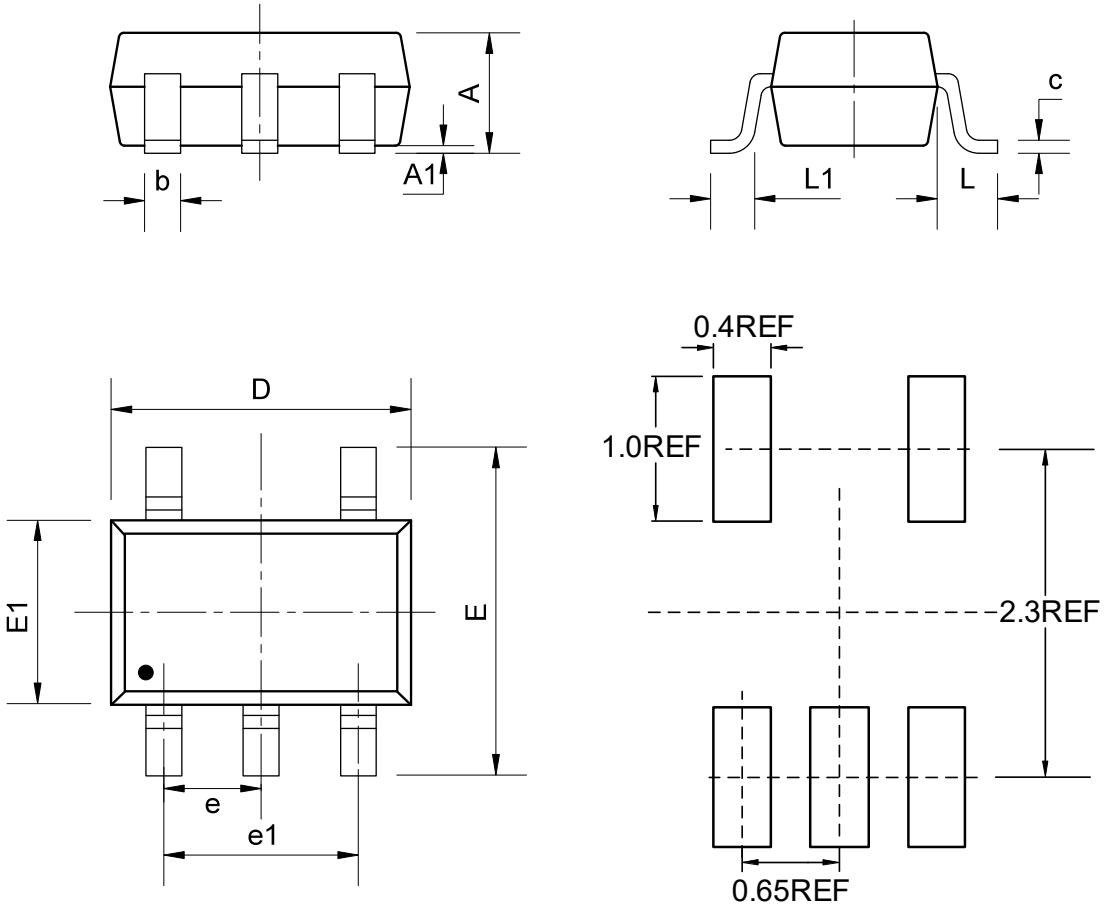


Figure4. Test Circuit R_T = 50Ω(Typ)

Package Dimension

SC70-5 (1.3mm × 2.1mm)

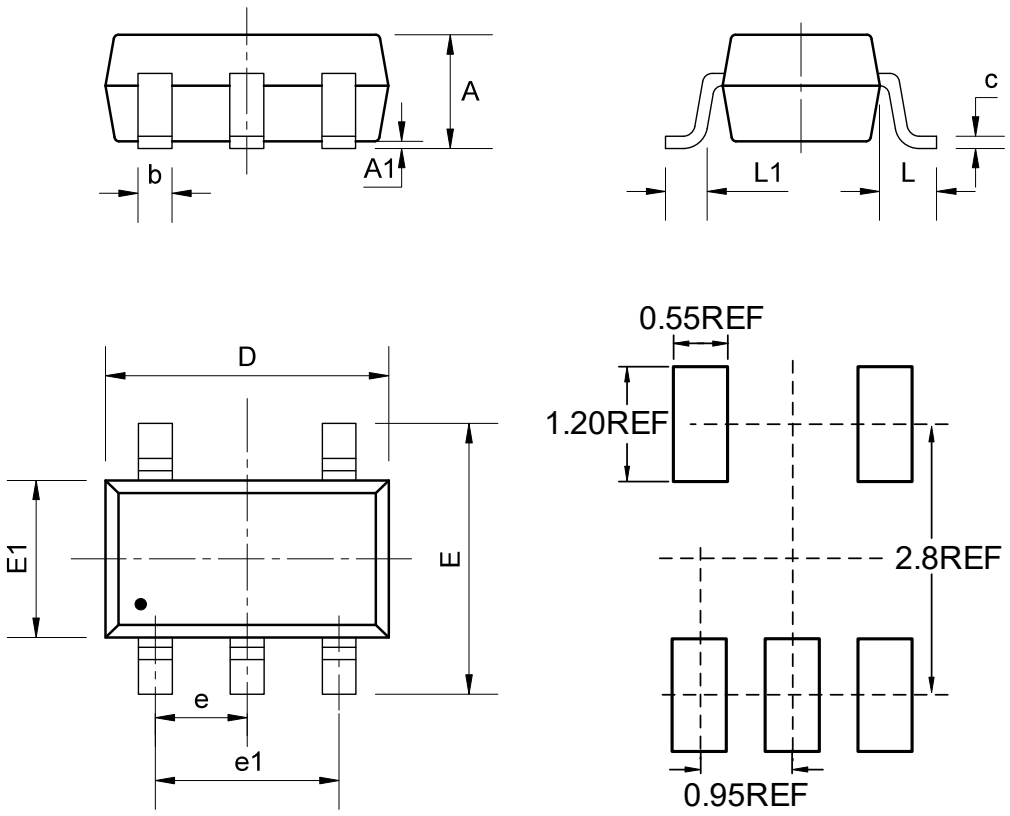


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.10
A1	0.00	-	0.15
b	0.15	-	0.35
c	0.08	-	0.20
D	2.00	2.10	2.30
e	0.65BSC		
e1	1.30BSC		
E	2.15	2.30	2.50
E1	1.15	1.30	1.45
L	0.50REF		
L1	0.33REF		

SOT23-5 (1.6mm × 2.9mm)

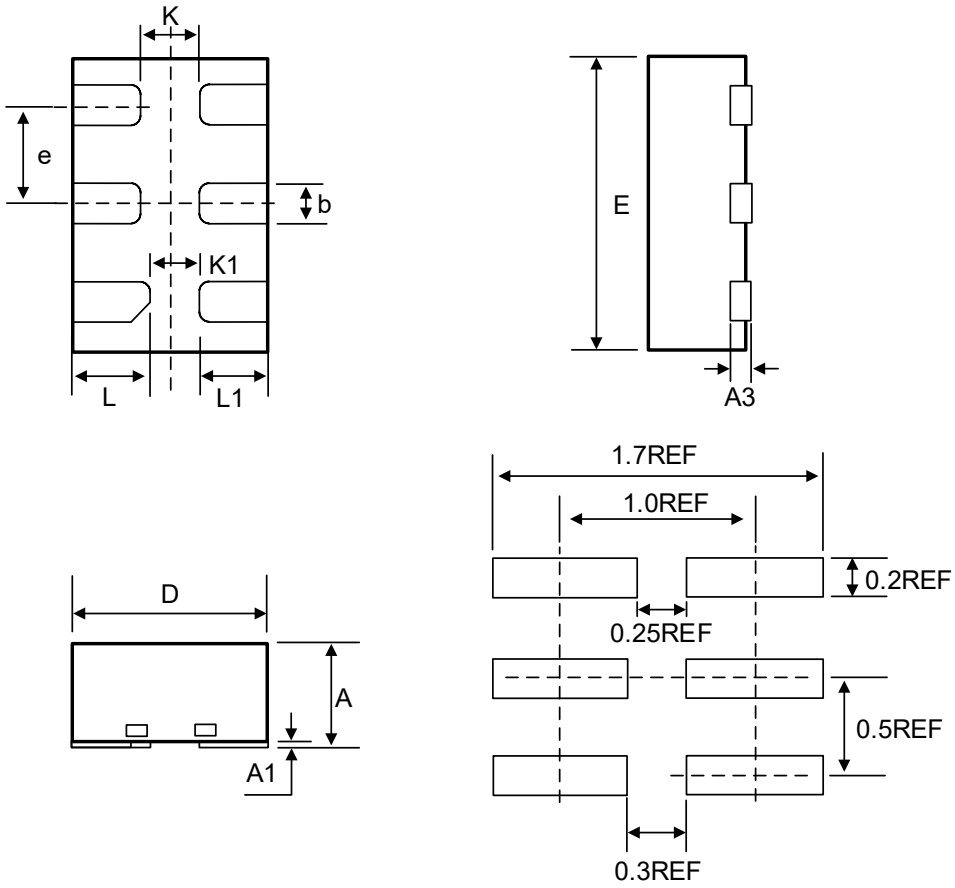


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.25
A1	0.00	-	0.15
b	0.36	-	0.50
c	0.12	-	0.20
D	2.75	2.9	3.05
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
E	2.60	2.80	3.00
E1	1.45	1.6	1.75
L	0.60REF		
L1	0.30	0.45	0.60

DFN6 (1.0mm × 1.5mm)



COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.50	--	0.60
A1	0.00	0.02	0.05
A3		0.10REF	
b	0.15	0.20	0.25
D	0.90	1.00	1.10
E	1.40	1.50	1.60
e		0.50BSC	
K		0.30REF	
K1		0.25REF	
L	0.35	0.40	0.45
L1	0.30	0.35	0.40

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2025-09-03	Official Version	Wang anran	Yang xiaoxu	Liu jiaying