

Single Buffer

General Description

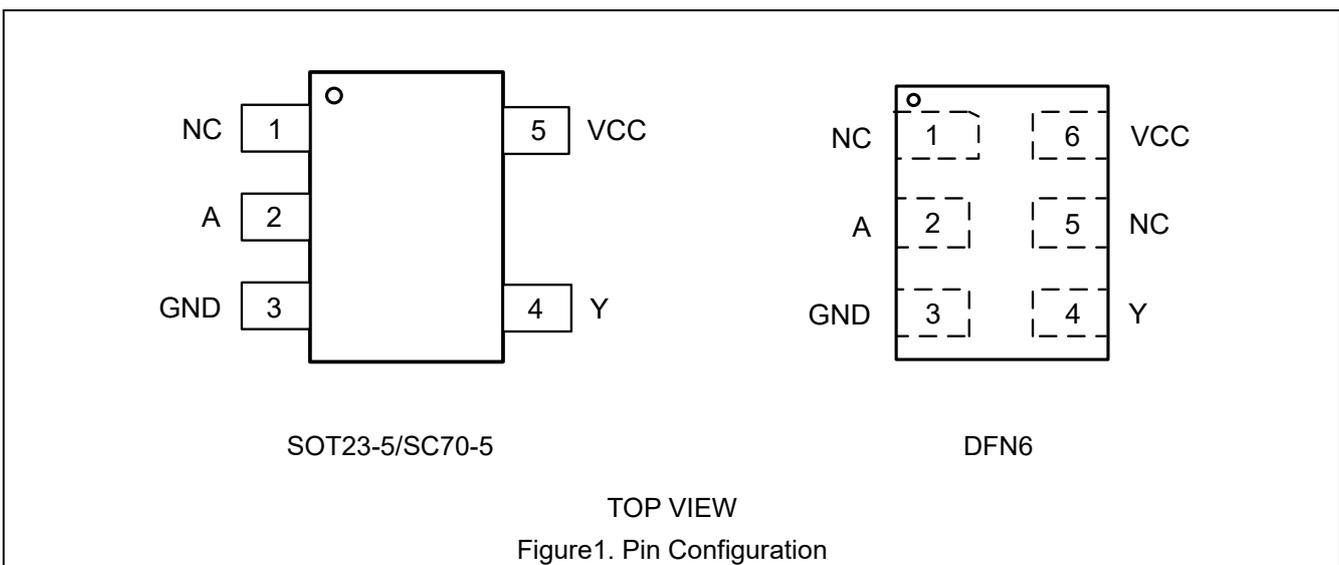
The ET74LV1T34 is a single buffer operating from a 1.6V to 5.5V supply. The device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive, while maintaining low static power dissipation.

Features

- $\pm 8\text{mA}$ Balanced Output Sink and Source Capability
- Over-Voltage Tolerant Inputs
- Designed for 1.6V to 5.5V V_{CC} Operation
- These Devices are Pb-Free and are RoHS Compliant Pin Configuration
- ESD Protection Complies with JESD22 Standard
 - HBM: $\pm 4000\text{V}$ Pass (JEDEC JS-001)
 - CDM: $\pm 1000\text{V}$ Pass (JEDEC JS-002)
- Latch-up Performance Exceeds $\pm 100\text{mA}$ per JEDEC JESD78F
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74LV1T34	SC70-5 (1.3mm \times 2.1mm)	Tape and Reel, 3K/Reel	Level 1
ET74LV1T34T	SOT23-5 (1.6mm \times 2.9mm)	Tape and Reel, 3K/Reel	Level 3
ET74LV1T34Y	DFN6 (1.0mm \times 1.5mm)	Tape and Reel, 3K/Reel	Level 1

Pin Configuration



ET74LV1T34

Pin Function

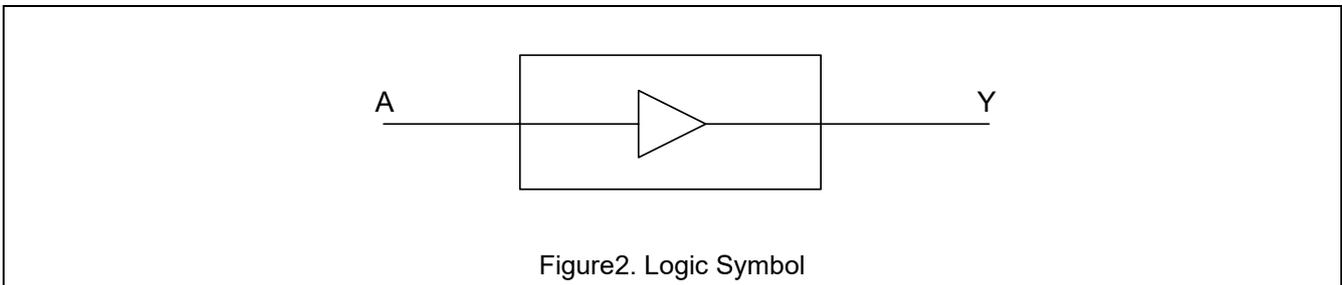
(SC70-5/ SOT23-5)

Pin No.	Pin Name	Function
1	NC	No Connect
2	A	Input
3	GND	Ground
4	Y	Output
5	VCC	Supply Voltage

DFN6

Pin No.	Pin Name	Function
1	NC	No Connect
2	A	Input
3	GND	Ground
4	Y	Output
5	NC	No Connect
6	VCC	Supply Voltage

Block Diagram



Functional Description

Function Table

Output Y = Input A	
Input A	Output Y
L	L
H	H

ET74LV1T34

Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V _{CC}	DC Supply Voltage(VCC Pin)		-0.5 to 7.0	V
V _I	DC Input Voltage ⁽¹⁾		-0.5 ≤ V _I ≤ 7.0	V
V _O	DC Output Voltage Output in Higher or Low State		-0.5 to V _{CC} + 0.5	V
I _{IK}	DC Input Diode Current V _I < GND		-50	mA
I _{OK}	DC Output Diode Current V _O < GND, V _O > V _{CC}		±50	mA
I _O	DC Output Sink Current		±50	mA
I _{CC}	DC Supply Current per Supply Pin		100	mA
I _{GND}	DC Ground Current per Supply Pin		-100	mA
T _{STG}	Storage Temperature Range		-65 to 150	°C
T _L	Lead Temperature, Soldering 10 Seconds		260	°C
T _J	Max Junction Temperature		150	°C
V _{ESD}	ESD Classification	Human Body Model ⁽²⁾	±4000	V
		Charged Device Model ⁽³⁾	±1000	
I _{LU}	Max Latch-up Current Above V _{CC} and GND at 125°C ⁽⁴⁾		±100	mA

Stresses exceeding those listed in this table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: I_O absolute maximum rating must be observed.

Note2: HBM tested per JEDEC JS-001;

Note3: CDM tested per JEDEC JS-002;

Note4: Latch up Current Maximum Rating tested per JEDEC JESD78F.

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
R _{θJA}	SC70-5	Thermal Characteristics, Thermal Resistance, Junction-to-Air	300	°C/W
	SOT23-5		250	
	DFN6		440	
P _D	SC70-5	Power Dissipation in Still Air at 85°C	215	mW
	SOT23-5		260	
	DFN6		150	

ET74LV1T34

Recommended Operating Conditions

Symbol	Parameter	Min	Max	Unit
V _{CC}	DC Supply Voltage Operating	1.6	5.5	V
V _{IN}	DC Input Voltage	0	5.5	V
V _{OUT}	DC Output Voltage (High or Low State)	0	5.5	V
T _A	Operating Temperature Range	-40	125	°C
t _r ,t _f	Input Rise and Fall Time	V _{CC} = 1.8V to 5.5V		ns/V

Electrical Characteristics

DC Electrical Characteristics

Symbol	Parameter	Condition	V _{CC} (V)	T _A = 25°C			-40°C ≤ T _A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
V _{IH}	High-level Input Voltage		1.65 to 1.8	0.94			1.0		V
			2.0	0.99			1.03		
			2.25 to 2.5	1.135			1.18		
			2.75	1.21			1.23		
			3.0 to 3.3	1.35			1.37		
			3.6	1.47			1.48		
			4.5 to 5.0	2.02			2.03		
			5.5	2.10			2.11		
V _{IL}	Low-level Input Voltage		1.65 to 2.0			0.45		0.40	V
			2.25 to 2.75			0.75		0.71	
			3.0 to 3.6			0.80		0.65	
			4.5 to 5.5			0.80		0.80	
V _{OH}	High-level Output Voltage	I _{OH} = -20μA	1.65 to 5.5	V _{CC} - 0.1	V _{CC}		V _{CC} - 0.1		V
		I _{OH} = -2mA	1.65	1.28			1.21		
		I _{OH} = -2mA	1.8	1.5			1.45		
		I _{OH} = -2.3mA	2.3	2.0			2.0		
		I _{OH} = -3mA	2.3	2.0			1.93		
		I _{OH} = -3mA	2.5	2.25			2.15		
		I _{OH} = -3mA	3.0	2.78			2.7		
		I _{OH} = -5.5mA	3.0	2.6			2.49		
		I _{OH} = -5.5mA	3.3	2.9			2.8		
		I _{OH} = -4mA	4.5	4.2			4.1		
		I _{OH} = -8mA	4.5	4.1			3.95		
			5.5	4.6			4.5		
V _{OL}	Low-level Output Voltage	I _{OL} = 20μA	1.65 to 5.5			0.1		0.1	V
		I _{OL} = 2mA	1.65			0.2		0.25	
		I _{OL} = 2.3mA	2.3			0.1		0.15	
		I _{OL} = 3mA	2.3			0.15		0.2	

ET74LV1T34

		$I_{OL} = 3\text{mA}$	3.0			0.1		0.15	
		$I_{OL} = 5.5\text{mA}$	3.0			0.2		0.252	
		$I_{OL} = 4\text{mA}$	4.5			0.15		0.2	
		$I_{OL} = 8\text{mA}$	4.5			0.3		0.35	
I_i	Input Leakage Current	$V_{IN} = 5.5\text{V}$ or GND	0 to 5.5			± 0.1		± 1.0	μA
I_{CC}	Quiescent Supply Current	$V_{IN} = 5.5\text{V}$ or GND	5.5			1.0		10	μA
ΔI_{CC}	Additional Supply Current	Per Input Pin; $V_I = 0.3\text{V}$ or 1.1V ; $I_O = 0\text{A}$; Other Pins at V_{CC} or GND	1.8			10		10	μA
		Per Input Pin; $V_I = 0.3\text{V}$ or 3.4V ; $I_O = 0\text{A}$; Other Pins at V_{CC} or GND	5.5			1.35		1.5	mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

AC Electrical Characteristics

Symbol	Parameter	Condition	$V_{CC}(\text{V})$	$T_A = 25^\circ\text{C}$			$-40^\circ\text{C} \leq T_A \leq 125^\circ\text{C}$		Unit
				Min	Typ	Max	Min	Max	
t_{PLH} t_{PHL}	Propagation Delay (Figure 3 & 4)	$C_L = 15\text{pF}$	1.8		12.2	15.4		17.4	ns
		$C_L = 30\text{pF}$	1.8		13.1	16.6		17.6	
		$C_L = 15\text{pF}$	2.5		8.4	10.4		12	
		$C_L = 30\text{pF}$	2.5		9.1	11.3		13	
		$C_L = 15\text{pF}$	3.3		6.7	8.3		9.4	
		$C_L = 30\text{pF}$	3.3		7.3	8.9		10.2	
		$C_L = 15\text{pF}$	5.0		5.6	6.5		7.3	
		$C_L = 30\text{pF}$	5.0		6	7		7.9	

ET74LV1T34

Capacitance Characteristics

Symbol	Parameter	Condition	Typ	Unit
C_{IN}	Input Capacitance	$V_{CC} = 5.5V, V_I = 0V \text{ or } V_{CC}$	5	pF
C_{PD}	Power Dissipation Capacitance ⁽⁵⁾	10MHz, $V_{CC} = 3.3V, V_I = 0V \text{ or } V_{CC}$	21	pF
		10MHz, $V_{CC} = 5.5V, V_I = 0V \text{ or } V_{CC}$	21	

Note5: C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o) \text{ where:}$$

f_i = input frequency in MHz;

f_o = output frequency in MHz;

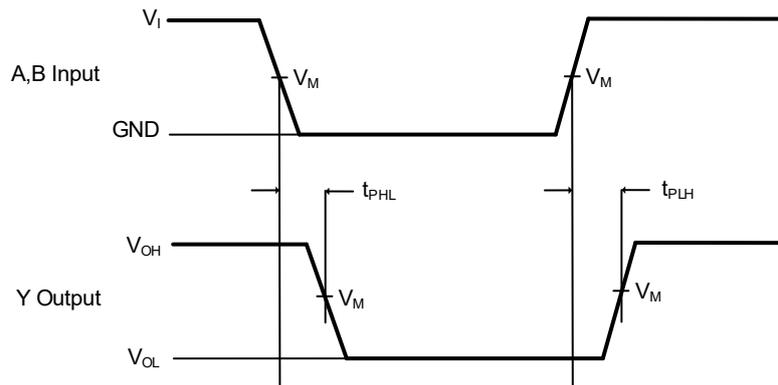
C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

AC Test Circuit



Measurement points are given in [Table 1](#).

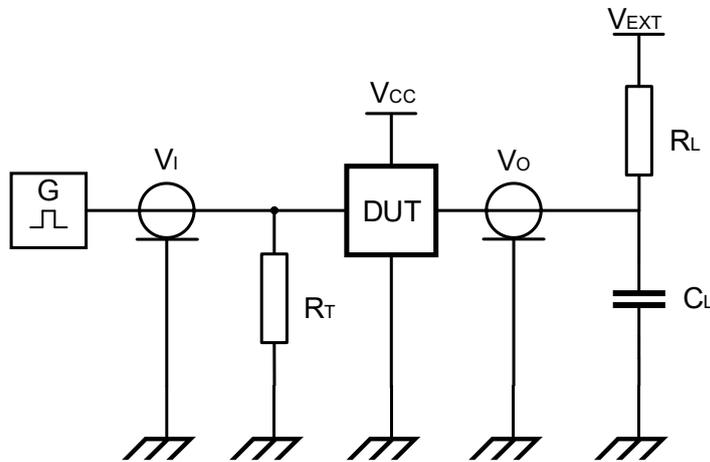
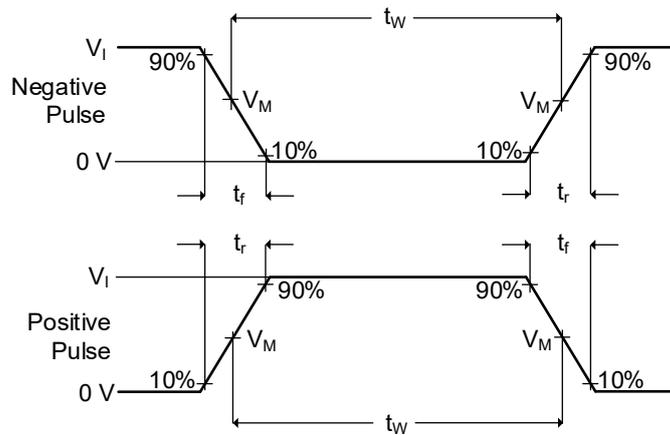
V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

Figure3. Input A, B and C to output Y propagation delay times

Table 1. Measurement Points

Input	Output
V_M	V_M
$0.5 \times V_I$	$0.5 \times V_{CC}$

ET74LV1T34



Measurement points are given in [Table 2](#).

Definitions test circuit:

R_L = Load resistance;

C_L = Load capacitance including jig and probe capacitance;

R_T = Termination resistance should be equal to output impedance Z_O of the pulse generator;

V_{EXT} = External voltage for measuring switching times.

Figure4. Test circuit for measuring switching times

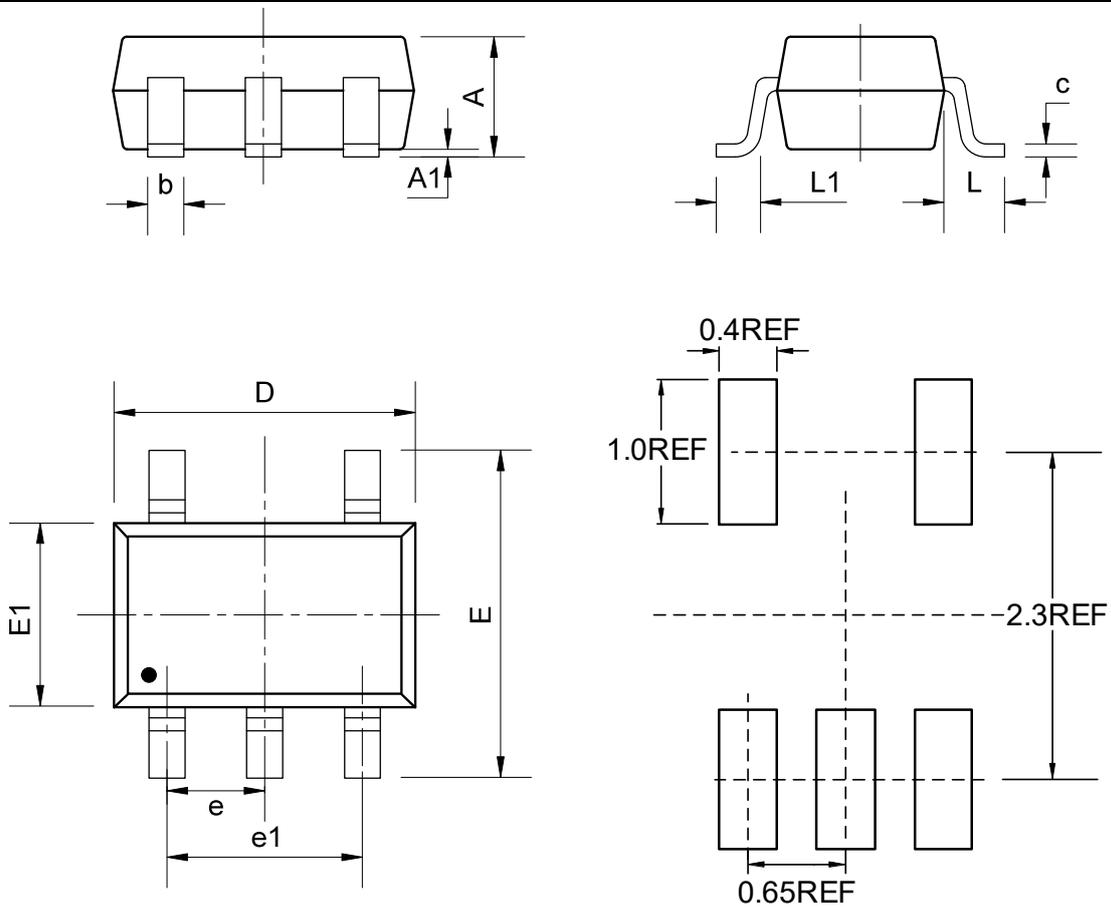
Table 2. Test Data

Supply Voltage		Input		Load		V_{EXT}
V_{CC}	V_I	$t_r = t_f$	f_{max}	C_L	R_L	t_{PLH}, t_{PHL}
1.8V	V_{CC}	$\leq 3ns$	15MHz	15pF, 30pF	1M Ω	open
2.5V	V_{CC}	$\leq 3ns$	25MHz	15pF, 30pF	1M Ω	open
3.3V	3V	$\leq 3ns$	50MHz	15pF, 30pF	1M Ω	open
5.0V	3V	$\leq 3ns$	50MHz	15pF, 30pF	1M Ω	open

ET74LV1T34

Package Dimension

SC70-5(1.3mm × 2.1mm)



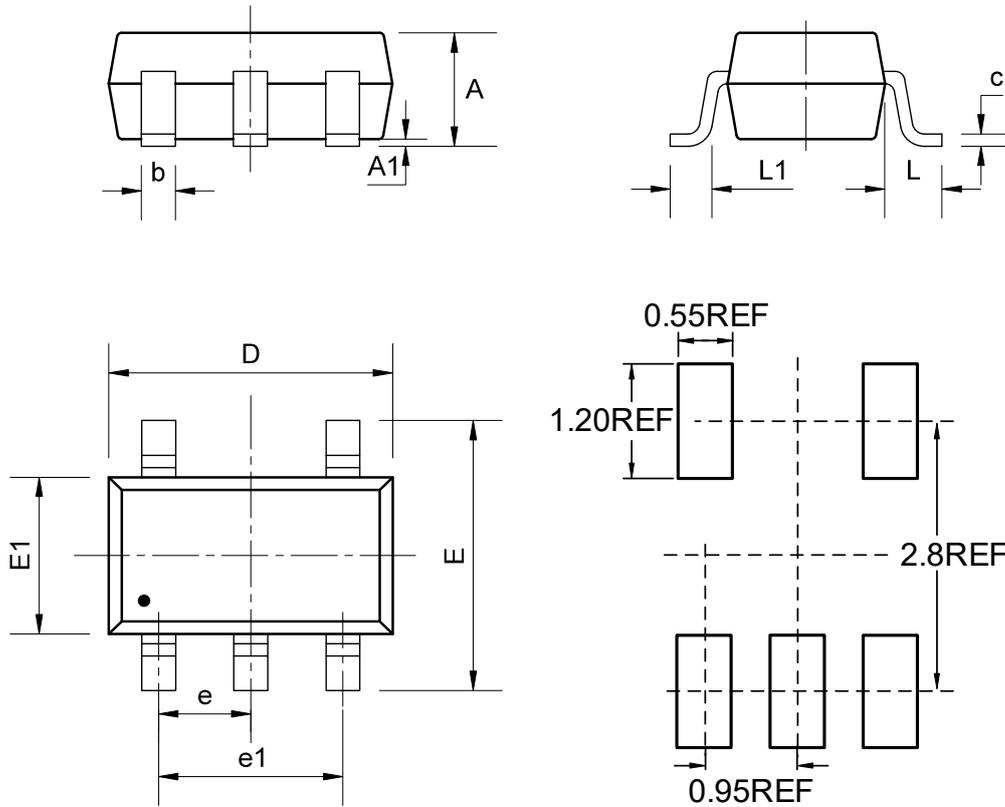
COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.05
A1	0	-	0.15
b	0.17	-	0.33
c	0.12	-	0.20
D	2.00	2.10	2.20
e	0.65BSC		
e1	1.30BSC		
E	2.20	2.30	2.40
E1	1.21	1.30	1.35
L	0.50REF		
L1	0.33REF		

ET74LV1T34

SOT23-5(1.6mm × 2.9mm)



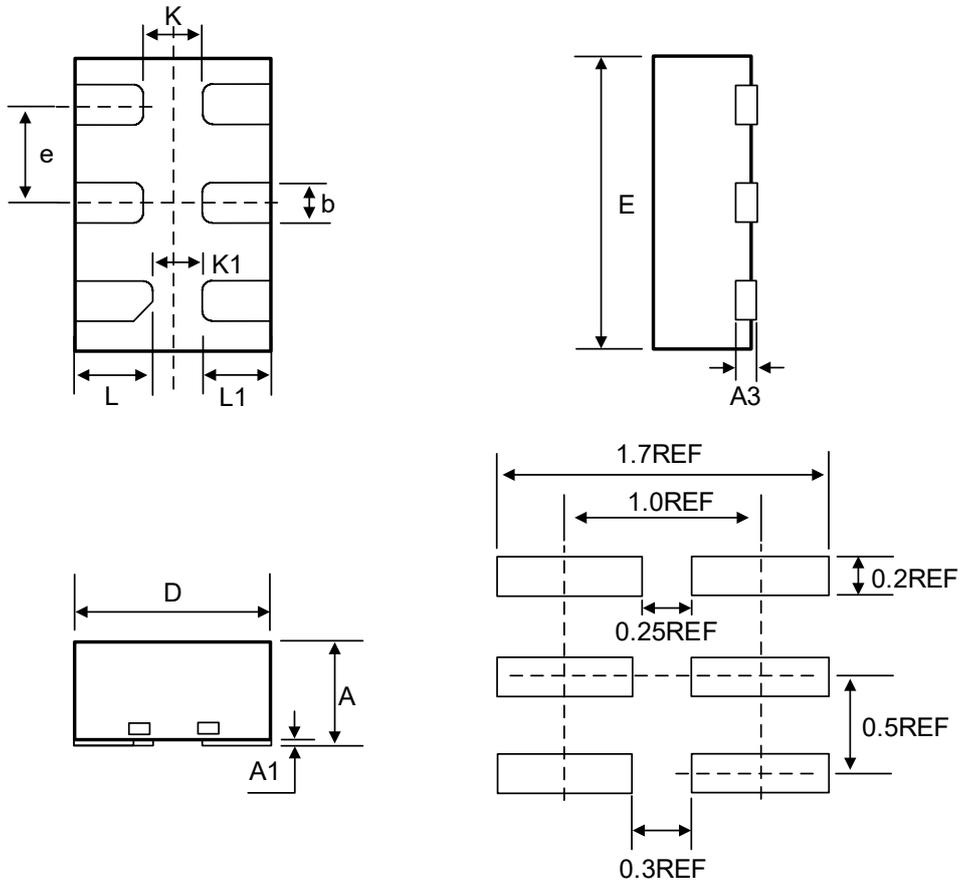
COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.25
A1	0	-	0.15
b	0.36	-	0.50
c	0.12	-	0.20
D	2.75	2.9	3.05
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
E	2.60	2.80	3.00
E1	1.45	1.6	1.75
L	0.60REF		
L1	0.30	0.45	0.60

ET74LV1T34

DFN6 (1.0mm × 1.5mm)



COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.34	0.37	0.40
A1	0	0.02	0.05
A3	0.10REF		
b	0.10	0.15	0.20
D	0.95	1.00	1.05
E	0.95	1.00	1.05
e	0.30	0.35	0.40
H	0.10REF		
K	0.15	--	--
L	0.35	0.40	0.45
L1	0.075REF		

ET74LV1T34

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2025-11-03	Official Version	Wang anran	Yang xiaoxu	Liu jiaoying