

Very High PSRR Low Noise 300mA LDO

General Description

The ET528XXYB family are the high performance 300mA LDO with auto discharge function, It uses an advanced CMOS process and a PMOSFET pass device to achieve very high power supply rejection ratio (PSRR), low noise, low dropout, low ground current, fast start-up and excellent output accuracy.

The ET528XXYB family are stable with a 1.0 μ F ceramic input and output capacitor, uses a precision voltage reference and feedback loop to achieve high excellent Regulation and transient response.

The ET528XXYB family are available in standard fixed output voltages of 1.1V (ET52811YB), 1.2V (ET52812YB), 1.8V (ET52818YB), 2.2V (ET52822YB), 2.8V (ET52828YB), 2.85V (ET528285YB), 3.0V (ET52830YB), 3.1V (ET52831YB), 3.3V (ET52833YB) and custom voltage options.

The ET528XXYB family are offered small DFN4(1x1) package, which is ideal for small form factor portable equipment.

Features

- Wide input voltage from 1.7V to 5.5V
- Up to 300mA load current
- Standard fixed output voltage options are 1.2V, 1.8V, 2.2V, 2.8V, 3.0V, 3.1V, and 3.3V etc.
- Other output voltage options available on request
- Very low I_Q is 45 μ A typical
- Low Dropout: 230mV at 300mA Load @ $V_{OUT} = 2.8V$
- Very high PSRR up to 78dB at 1kHz @ $V_{OUT} = 2.8V$
- low noise is 35 μ Vrms
- With auto discharge function
- OTP, OCP, SCP
- Part No. and Package

Part No.	Package	MSL
ET528XXYB	DFN4(1mm × 1mm)	1

Applications

- Smart Phones and Cellular Phones
- PDAs
- Digital Still Cameras
- Portable instrument
- Bluetooth, wireless handsets

ET528XXYB

Mark Specification

Part No.	Marking	V _{OUT}	Auto Discharge Function
ET52812YB	AX	1.2V	Y
ET52818YB	CX	1.8V	Y
ET52822YB	QX	2.2V	Y
ET52828YB	DX	2.8V	Y
ET528285YB	HX	2.85V	Y
ET52830YB	GX	3.0V	Y
ET52831YB	ZX	3.1V	Y
ET52833YB	EX	3.3V	Y

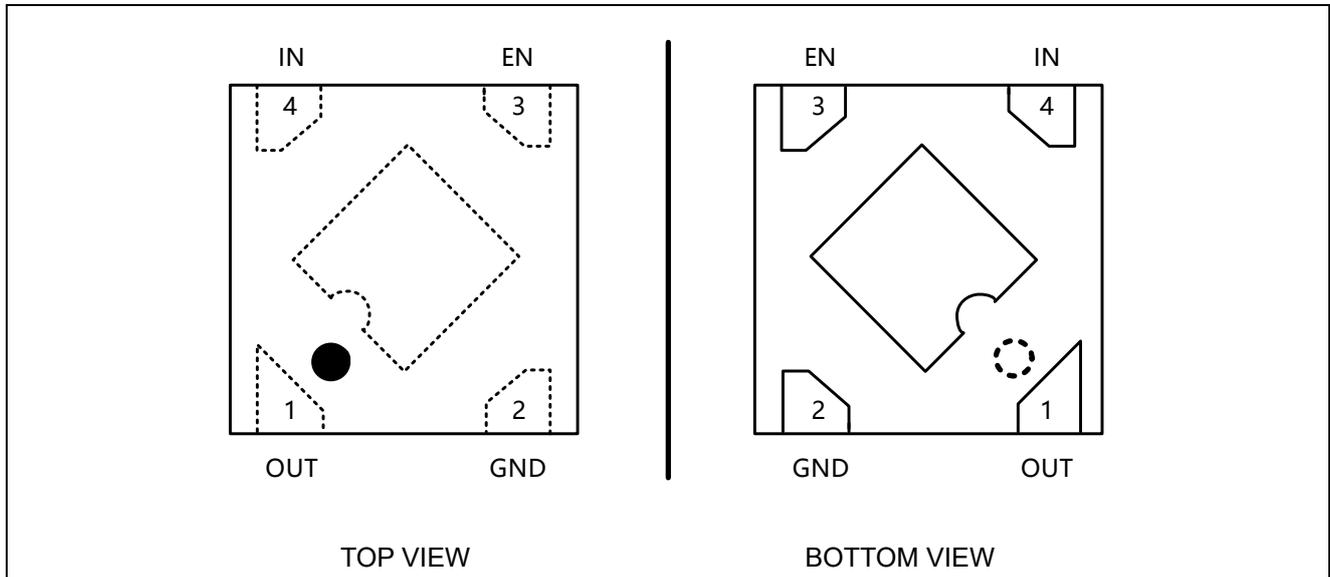
Device information

ET 528 XX X B

<u>XX</u> Output Voltage		<u>X</u> Package		<u>B</u> Auto-discharge Function	
XX	V _{OUT} =X.XV	Y	DFN4-1.0x1.0	B	Auto-discharge
				/	None

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Pin Configuration

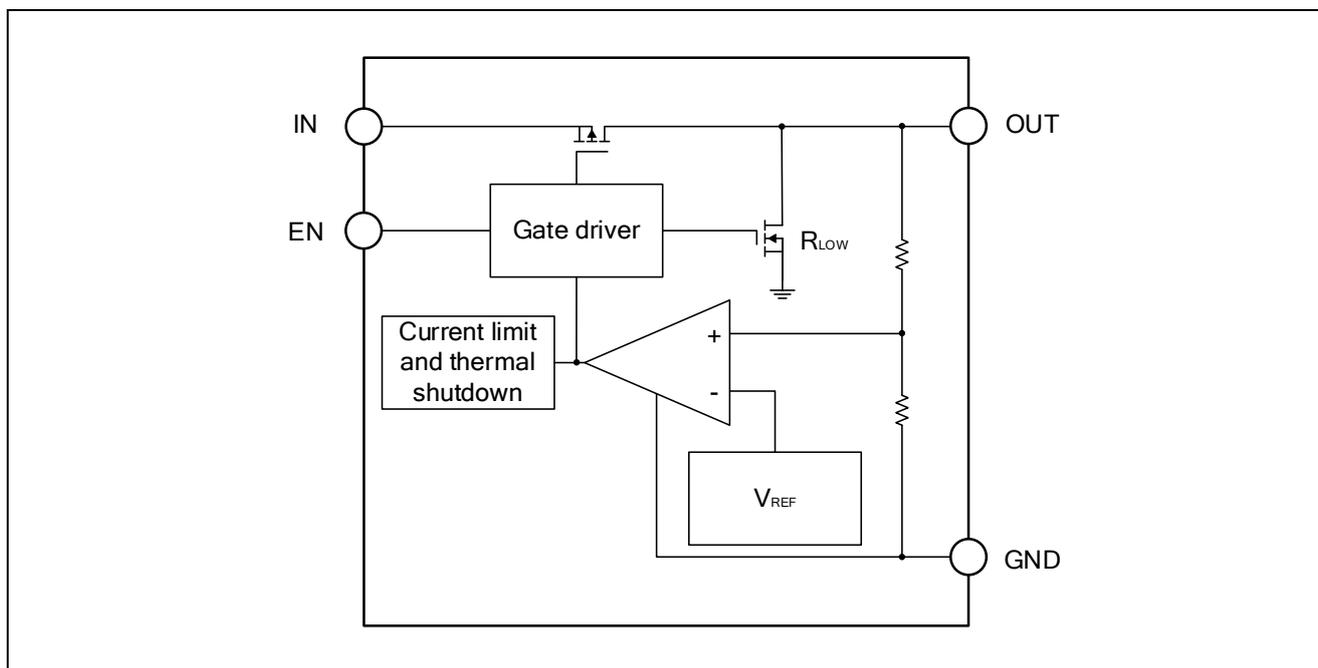


Pin Function

Pin No.	Pin Name	Pin Function
1	OUT	Output pin. Bypass a 1 μ F ceramic capacitor from this pin to ground.
2	GND	Ground pin.
3	EN	Enable control input, active high. Do not leave EN floating.
4	IN	Supply input pin. Must be closely decoupled to GND with a 1 μ F or greater ceramic capacitor.
	Thermal Pad	Thermal pad connect to GND or leave floating. Do not connect to any potential other than GND.

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Block Diagram



Functional Description

Input Capacitor

A 1 μ F ceramic capacitor is recommended to connect between V_{IN} and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both V_{IN} and GND.

Output Capacitor

An output capacitor is required for the stability of the LDO. The recommended output capacitance is 1 μ F or larger, Equivalent Series Resistance (ESR) is from 5m Ω to 100m Ω , and temperature characteristics are X7R or X5R. Higher capacitance values help to improve load/line transient response. The output capacitance may be increased to keep low undershoot/overshoot. Place output capacitor as close as possible to OUT and GND pins.

ON/OFF Input Operation

The ET528XXYB is turned on by setting the EN pin high, and is turned off by pulling it low. If this feature is not used, the EN pin should be tied to IN pin to keep the regulator output on at all time.

High PSRR and Low Noise

In order to provide good audio quality, the audio power supply for hand-free, game, MP3, and multimedia applications in cellular phones, require low-noise and high PSRR at audio frequency range (20Hz-20kHz).

The ET528XXYB, with PSRR of 78dB at 1KHz @ $V_{OUT} = 2.8V$, is suitable for most of these applications that require high PSRR and low noise.

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Fast Transient Response

Fast transient response LDOs can also extend battery life. TDMA-based cell phone protocols such as Global System for Mobile Communications (GSM) have a transmit/receive duty factor of only 12.5 percent, enabling power savings by putting much of the baseband circuitry into standby mode in between transmit cycles. In baseband circuits, the load often transitions virtually instantaneously from 100 μ A to 100mA. To meet this load requirement, the LDO must react very quickly without a large voltage drop or overshoot - a requirement that cannot be met with conventional, general-purpose LDOs.

The ET528XXYB's fast transient response from 1 to 300mA provides stable voltage supply for fast DSP and GSM chipset with fast changing load.

Low Quiescent Current

The ET528XXYB consuming only around 48 μ A for all input range and output loading, provides great power saving in portable and low power applications.

Current Limit Protection

When output current at the OUT pin is higher than current limit threshold or the OUT pin is short-circuit to GND, the current limit protection will be triggered and clamp the output current to approximately 500mA to prevent over-current and to protect the regulator from damage due to overheating.

Thermal Shutdown Protection

Thermal protection disables the output when the junction temperature rises to approximately +155 $^{\circ}$ C, allowing the device to cool down. When the junction temperature reduces to approximately +130 $^{\circ}$ C the output circuitry is enabled again. Depending on power dissipation, thermal resistance, and ambient temperature, the thermal protection circuit may cycle on and off. This cycling limits the heat dissipation of the regulator, protecting it from damage due to overheating.

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Absolute Maximum Ratings

Symbol	Parameters (Items)	Value	Unit
V _{IN}	IN Voltage	-0.3 to 6	V
V _{EN}	Input Voltage (EN Pin)	-0.3 to 6	V
V _{OUT}	Output Voltage	-0.3 to V _{IN} +0.3	V
I _{MAX}	Maximum Load Current	300	mA
P _D	Maximum Power Consumption	DFN4	400 mW
ESD	Human Body Model (JEDEC JS-001)	±4000	V
	Charged Device Model(JESD22-C101)	±1500	V
R _{θJA}	Junction-to-ambient thermal resistance	250	°C/W
T _J	Operating Junction Temperature	-40 to 150	°C
T _{STG}	Storage Temperature	-65 to 150	°C
T _{SLOD}	Lead Temperature (Soldering, 10 sec)	300	°C

Recommended Operating Conditions

Symbol	Item	Rating	Unit
V _{IN}	Input Voltage	1.7 to 5.5	V
I _{OUT}	Output Current	0 to 300	mA
T _A	Operating Ambient Temperature	-40 to 85	°C
C _{IN}	Effective Input Ceramic Capacitor Value	0.47 to 4.7	μF
C _{OUT}	Effective Output Ceramic Capacitor Value	0.47 to 4.7	μF
ESR	Input and Output Capacitor Equivalent Series Resistance	5 to 100	mΩ

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Electrical Characteristics ⁽¹⁾

($V_{IN}=V_{EN}=V_{OUT}+1.0V$, $I_{OUT}=1mA$, $C_{IN}=1\mu F$, $C_{OUT}=1\mu F$, Typical values are at $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage Operation Range	V_{IN}		1.7		5.5	V
Dropout Voltage ⁽²⁾	V_{DROP}	$V_{OUT} = 1.2V, I_{OUT} = 300mA$		460	700	mV
		$V_{OUT} = 1.8V, I_{OUT} = 300mA$		270	420	
		$V_{OUT} = 2.8V, I_{OUT} = 300mA$		230	345	
		$V_{OUT} = 3.0V, I_{OUT} = 300mA$		220	340	
		$V_{OUT} = 3.3V, I_{OUT} = 300mA$		210	330	
DC Supply Quiescent Current	I_{Q_ON}	Active mode: $V_{EN} = V_{IN}$		45	80	μA
DC Supply Shutdown Current	I_{Q_OFF}	$V_{EN} = 0V$		0.01	1	μA
Regulated Output Voltage	V_{OUT}	$V_{OUT} \geq 2.0V, V_{IN}=V_{OUT}+1V, I_{OUT}=1mA, T_A=25^\circ C$	-1		1	%
		$V_{OUT} < 2.0V, V_{IN}=V_{OUT}+1V, I_{OUT}=1mA, T_A=25^\circ C$	-30		30	mV
Output Voltage Line Regulation	Reg_{LINE}	$V_{IN} = V_{OUT} + 1V$ to $5.5V, I_{OUT} = 10mA$			0.2	%/V
Output Voltage Load Regulation	Reg_{LOAD}	I_{OUT} from $1mA$ to $300mA, T_A=25^\circ C$		20	40	mV
Soft-start Time	T_{ON}	From enable to power on, $V_{OUT}=1.8V$		50		μs
Current Limit	I_{LIMIT}		450			mA
Short Circuit Current Limit	I_{SHORT}	$V_{OUT} = 0V$		70		mA
Power Supply Rejection Ratio	PSRR	$V_{IN} = V_{IN-set} \pm 200mV_{pp}, C_{OUT} = 1\mu F, I_{OUT} = 10mA, V_{OUT} = 2.8V$	$f=1kHz$		78	dB
			$f=100kHz$		55	dB
			$f=1MHz$		50	dB
Output Noise	e_{N}	$10Hz$ to $100kHz, I_{OUT} = 100mA, C_{OUT} = 1\mu F$		35		μV_{RMS}
EN Low Threshold	V_{ENL}				0.4	V
EN High Threshold	V_{ENH}		0.9			V
Output Discharge FET R_{dson}	R_{LOW}			145		Ω
EN pull-down Resistance	R_{EN}			1		$M\Omega$

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Electrical Characteristics(Continued)

($V_{IN}=V_{EN}=V_{OUT}+1.0V$, $I_{OUT}=1mA$, $C_{IN}=1\mu F$, $C_{OUT}=1\mu F$, Typical values are at $T_A=25^\circ C$ unless otherwise noted)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Over-temperature Shutdown Threshold	T_{TSD}	T_J rising		155		$^\circ C$
Over-temperature Shutdown Hysteresis	T_{HYS}	T_J falling from shutdown		25		$^\circ C$

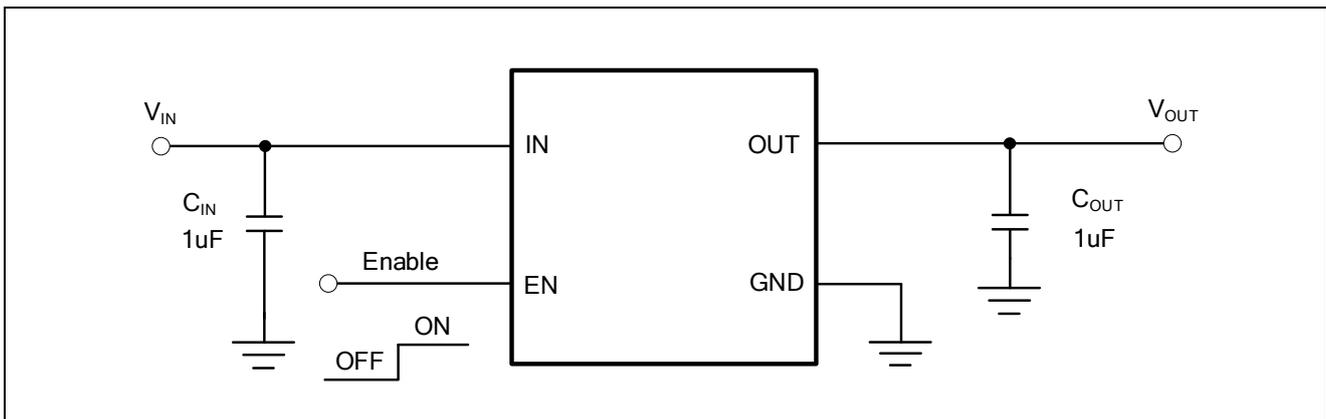
Notes:

1: Production test at $+25^\circ C$. Specifications over the temperature range are guaranteed by design and characterization.

2: The minimum operating voltage is 1.7V. The calculation formula is as follows:

$$V_{DROP}=V_{IN(min)}-V_{OUT}$$

Application Circuits

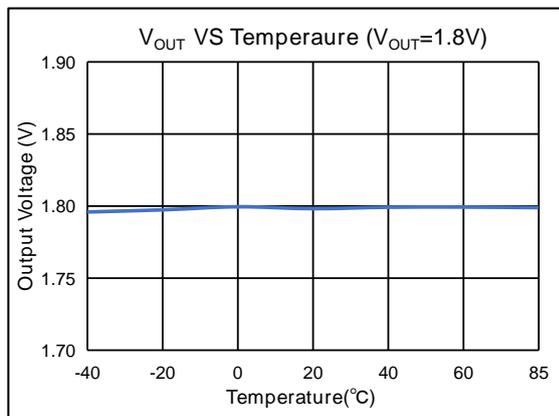


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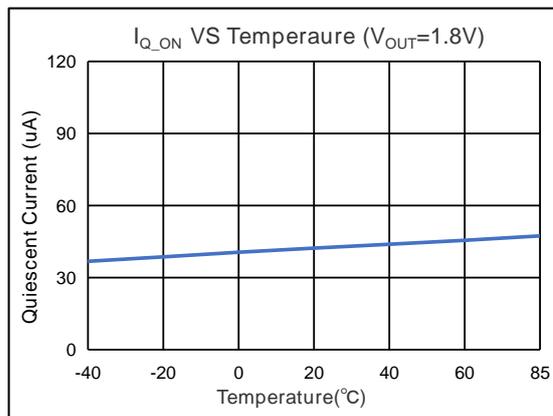
Typical Characteristics

(1) VOLTAGE VERSION 1.8 V

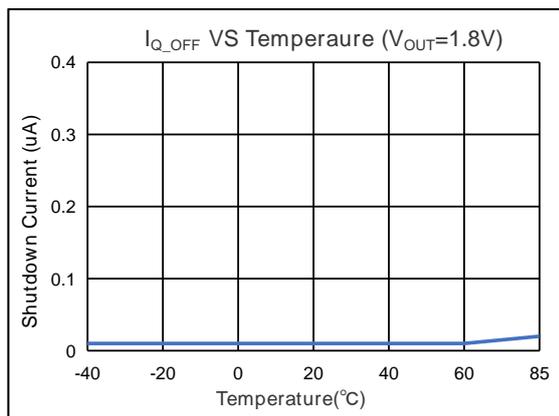
($V_{IN}=V_{EN}=2.8V$; $I_{OUT}=1mA$, $C_{IN}=C_{OUT}=1.0\mu F$, unless otherwise noted. Typical values are at $T_A=25^\circ C$.)



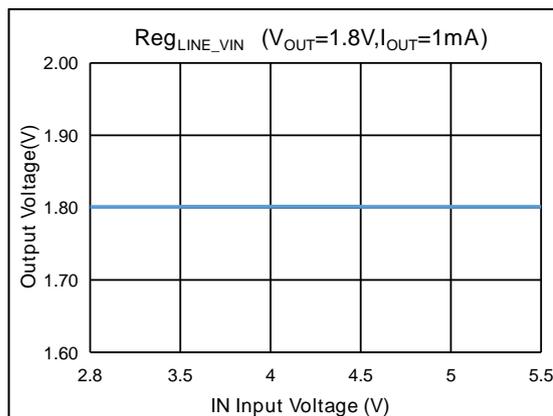
Output Voltage VS Temperature



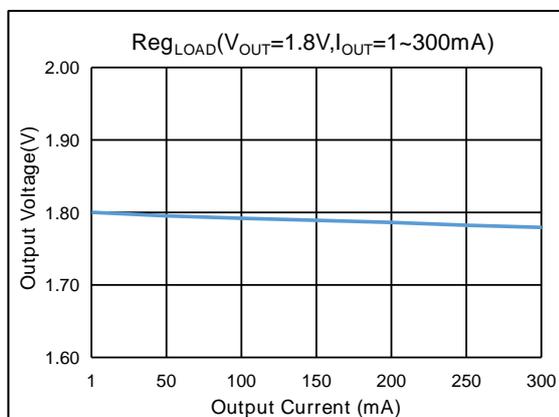
Supply Current VS Temperature



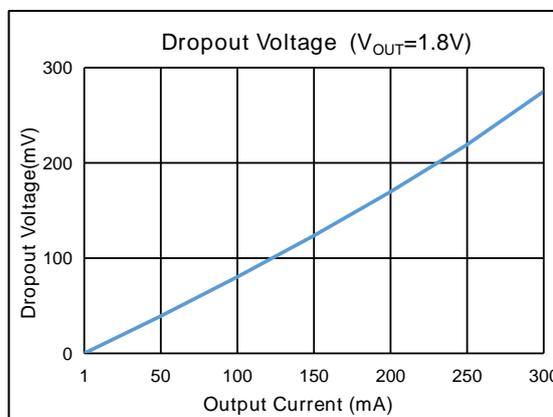
Standby Current VS Temperature



Output Voltage VS Input Voltage (I_{OUT}=1mA)



Output Voltage VS Output Current



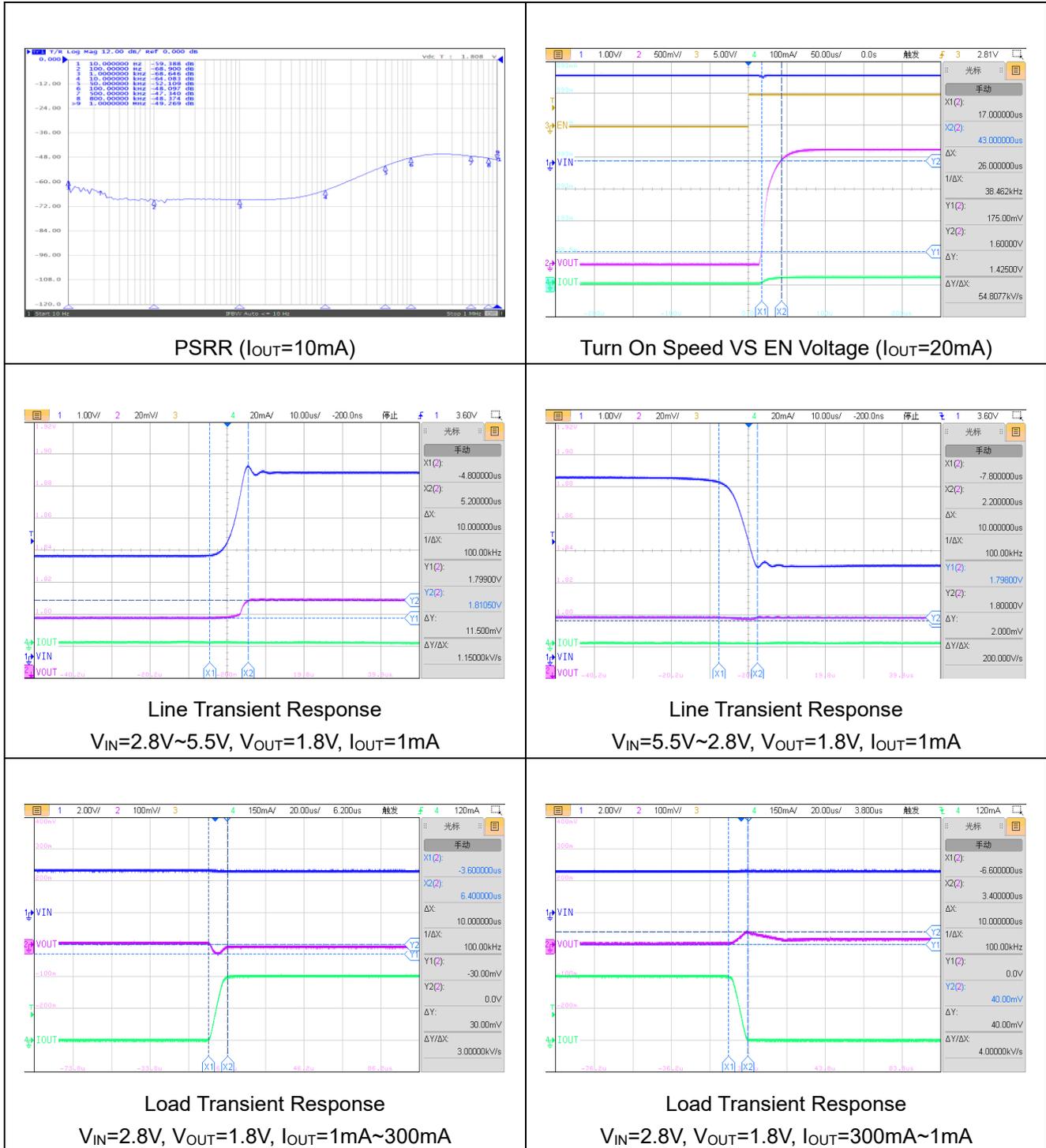
Dropout Voltage VS Output Current

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Typical Characteristics(Continued)

(1) VOLTAGE VERSION 1.8 V

($V_{IN}=V_{EN}=2.8V$; $I_{OUT}=1mA$, $C_{IN}=C_{OUT}=1.0\mu F$, unless otherwise noted. Typical values are at $T_A=25^\circ C$.)

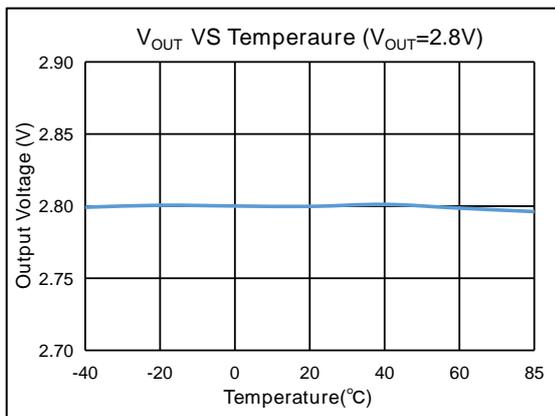


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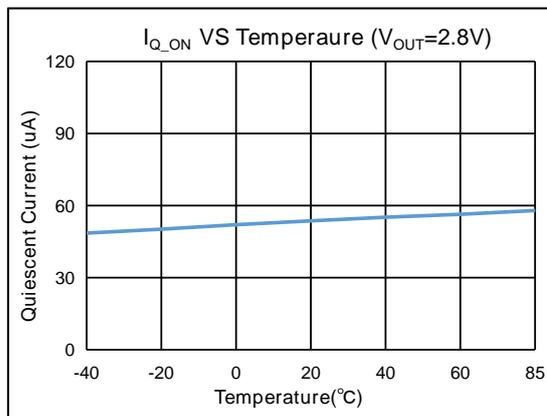
Typical Characteristics(Continued)

(2) VOLTAGE VERSION 2.8 V

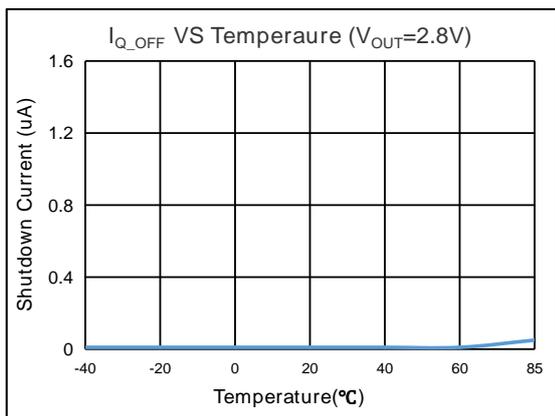
($V_{IN}=V_{EN}=3.8V$; $I_{OUT}=1mA$, $C_{IN}=C_{OUT}=1.0\mu F$, unless otherwise noted. Typical values are at $T_A=25^\circ C$.)



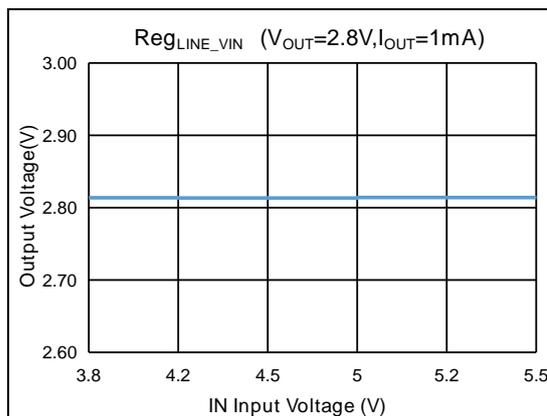
Output Voltage VS Temperature



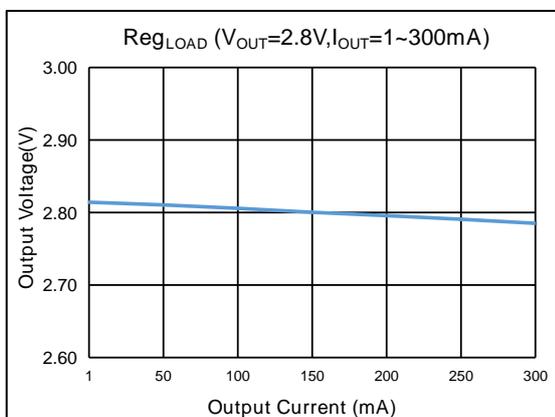
Supply Current VS Temperature



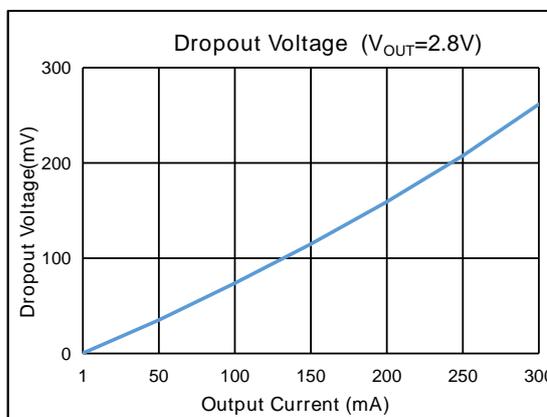
Standby Current VS Temperature



Output Voltage VS Input Voltage(I_{OUT}=1mA)



Output Voltage VS Output Current



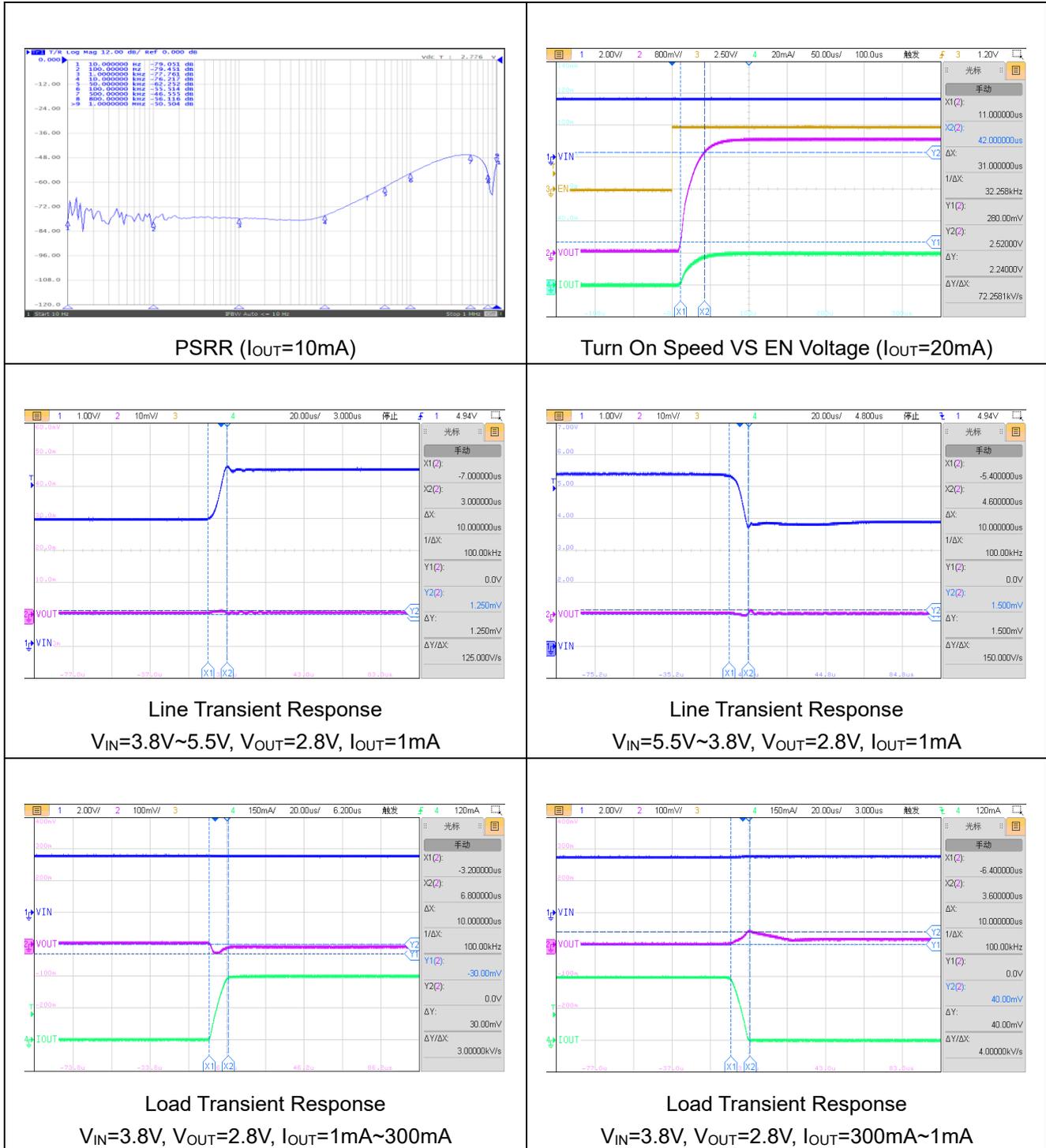
Dropout Voltage VS Output Current

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Typical Characteristics (Continued)

(2) VOLTAGE VERSION 2.8 V

($V_{IN}=V_{EN}=3.8V$; $I_{OUT}=1mA$, $C_{IN}=C_{OUT}=1.0\mu F$, unless otherwise noted. Typical values are at $T_A=25^\circ C$.)

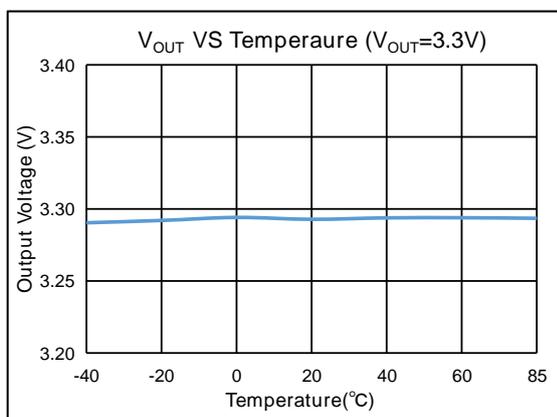


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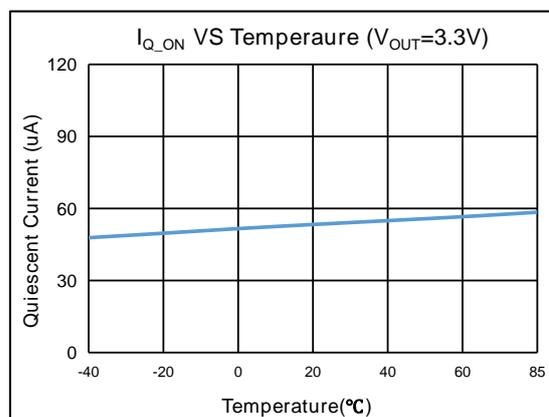
Typical Characteristics (Continued)

(3) VOLTAGE VERSION 3.3 V

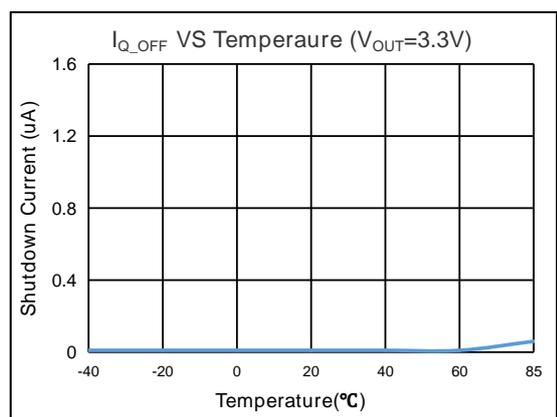
($V_{IN}=V_{EN}=4.3V$; $I_{OUT}=1mA$, $C_{IN}=C_{OUT}=1.0\mu F$, unless otherwise noted. Typical values are at $T_A=25^\circ C$.)



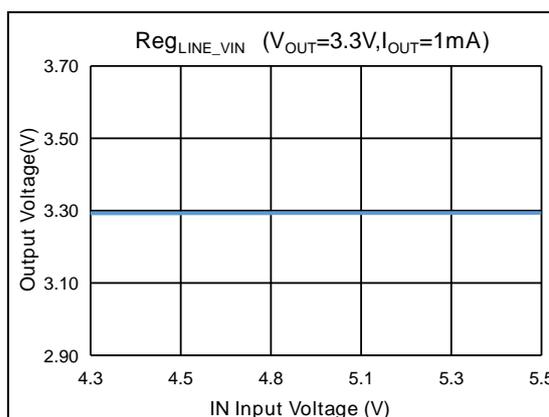
Output Voltage VS Temperature



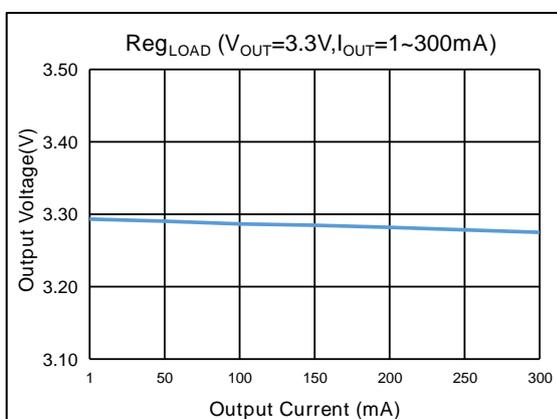
Supply Current VS Temperature



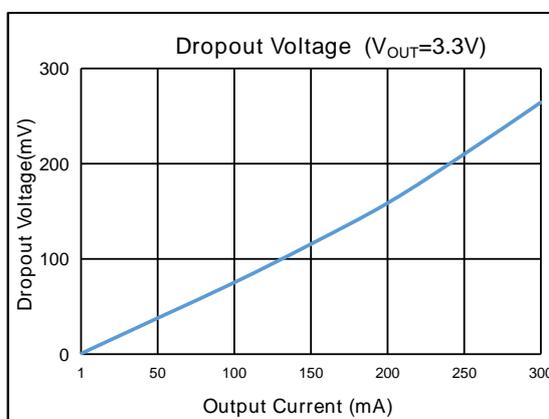
Standby Current VS Temperature



Output Voltage VS Input Voltage (I_{OUT}=1mA)



Output Voltage VS Output Current



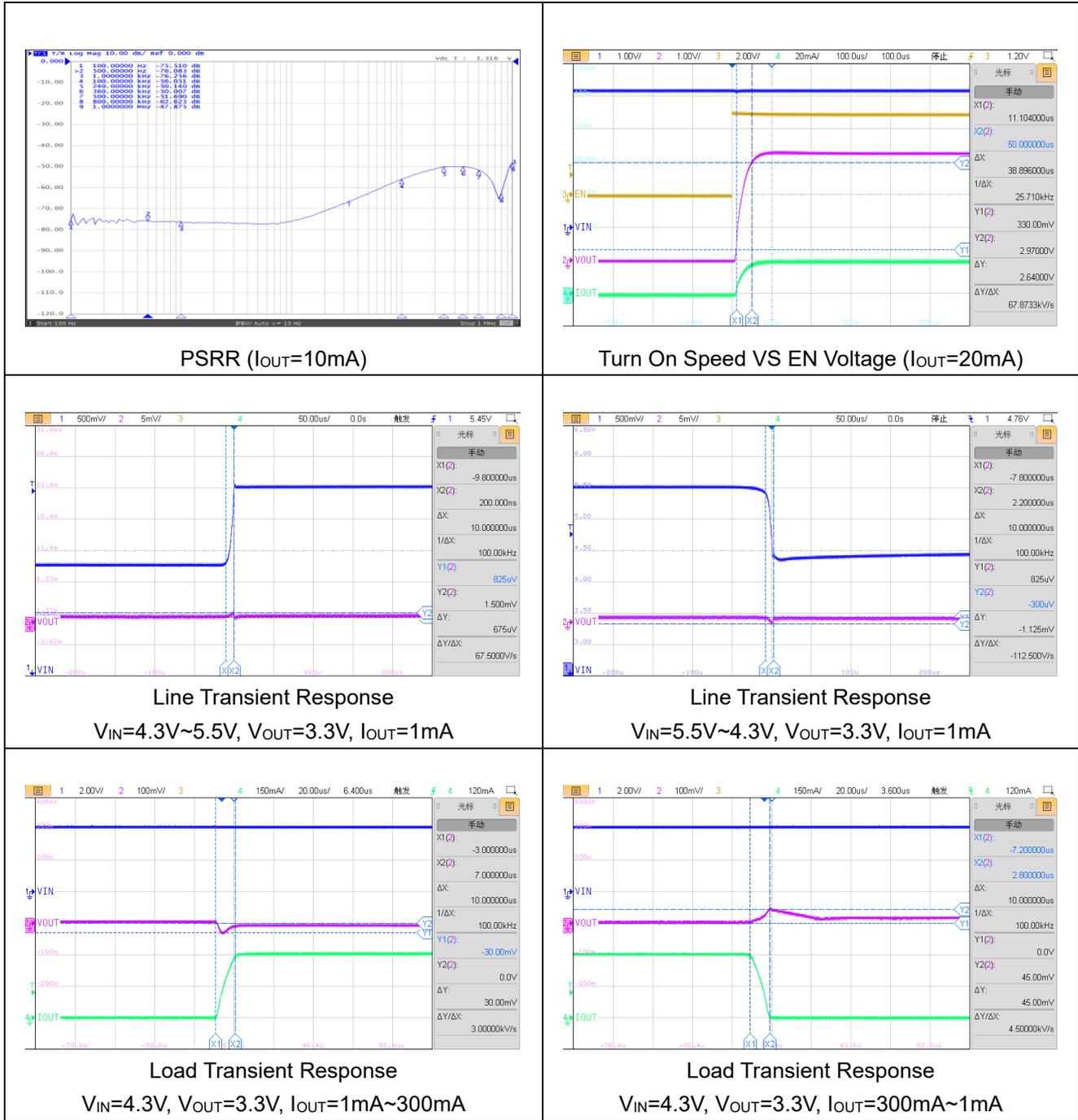
Dropout Voltage VS Output Current

ET528XXYB

Typical Characteristics (Continued)

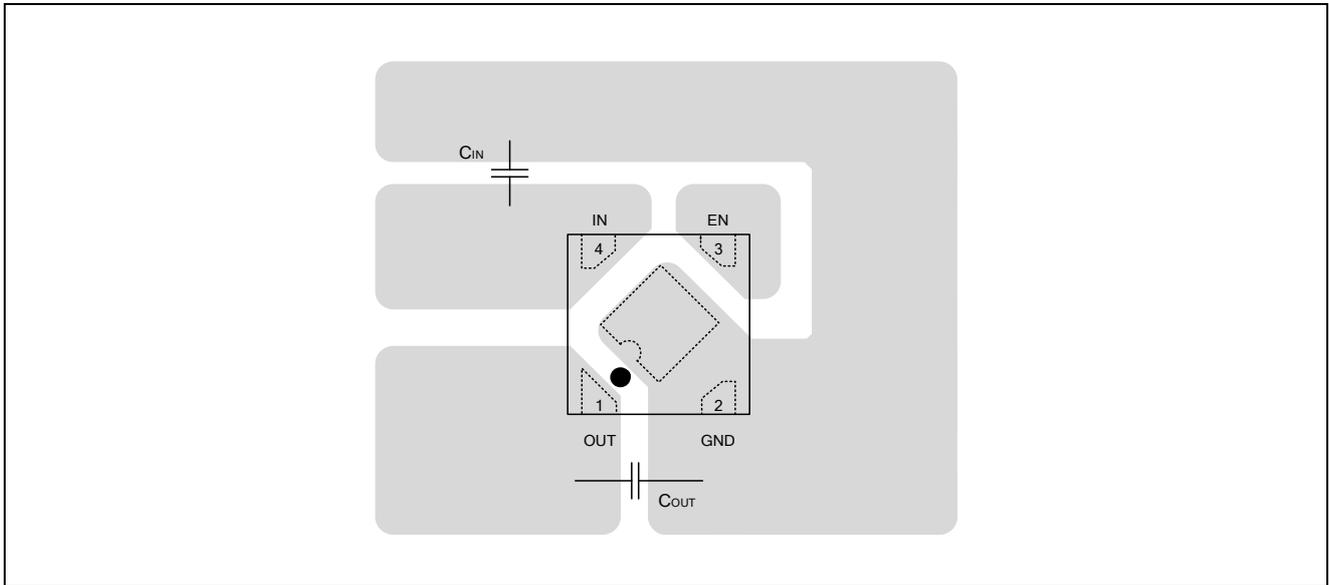
(3) VOLTAGE VERSION 3.3 V

($V_{IN}=V_{EN}=4.3V$; $I_{OUT}=1mA$, $C_{IN}=C_{OUT}=1.0\mu F$, unless otherwise noted. Typical values are at $T_A=25^\circ C$.)

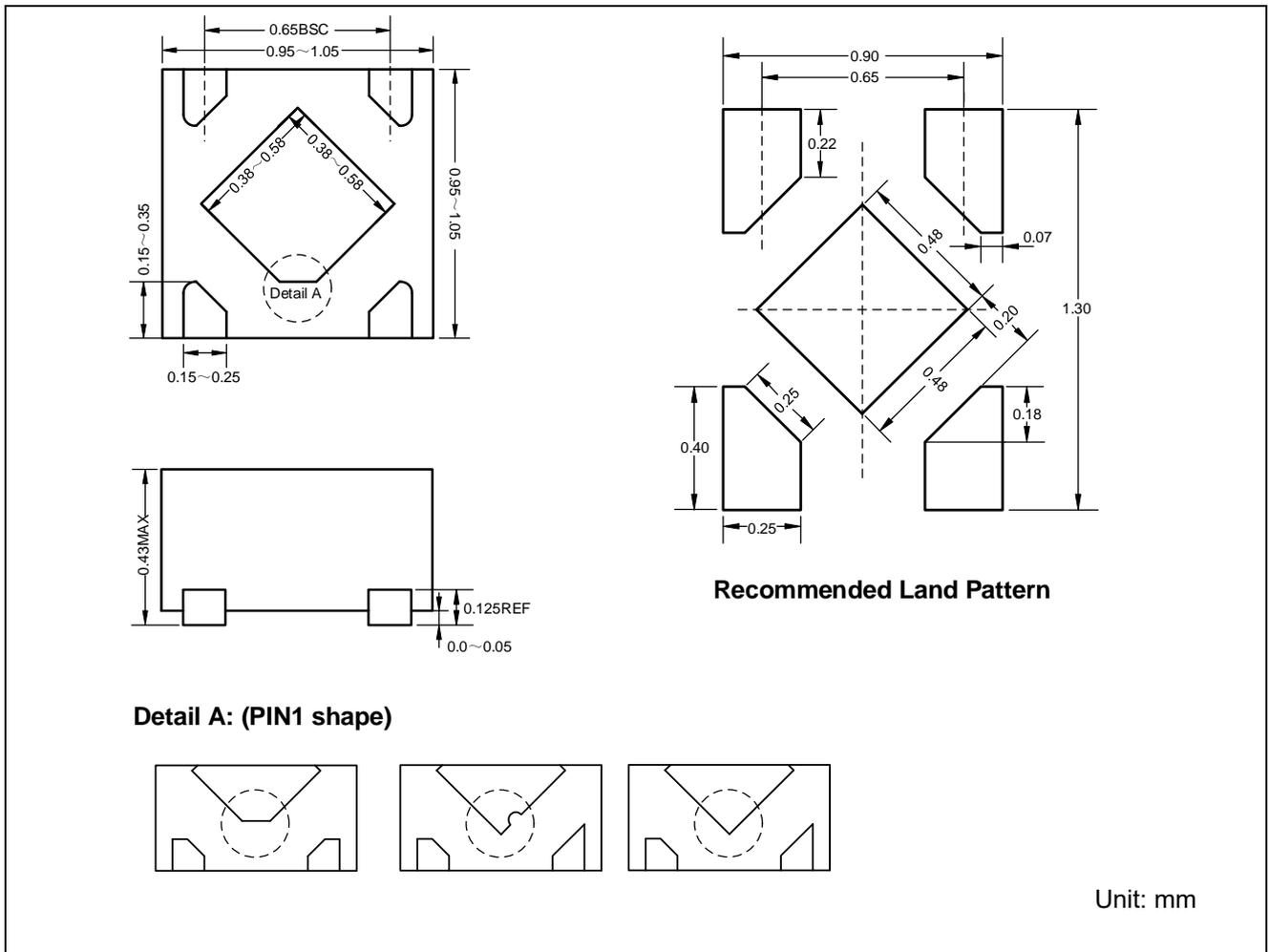


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PCB Layout Guide

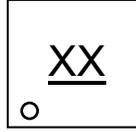


Package Dimension



ET528XXYB

Marking



X = Output Voltage

X = Tracking Number

