Single 2-Input AND Gate

General Description

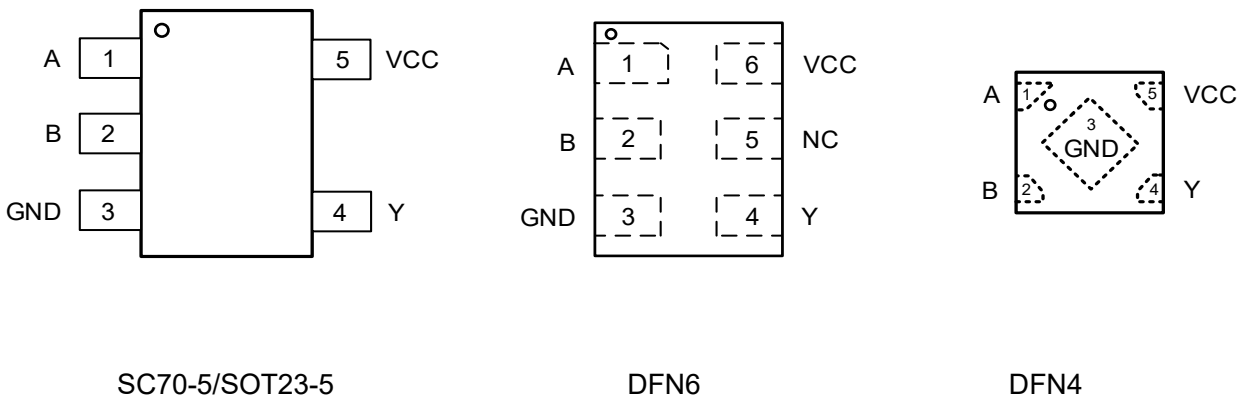
The ET74LVC1G08 is a single 2-input AND Gate operating from a 1.65V to 5.5V supply. This device is fabricated with advanced CMOS technology to achieve ultra-high speed with high output drive.

Features

- Designed for 1.65V to 5.5V V_{CC} Operation
- Over-Voltage Tolerant Inputs
- ±32mA Balanced Output Sink and Source Capability
- Near Zero Static Supply Current Substantially Reduces System Power Requirements
- These Devices are Pb-Free and RoHS Compliant
- ESD Protection Complies with JESD22 Standard
  -- HBM: ±4000V Pass (JEDEC JS-001)
  -- CDM: ±1000V Pass (JEDEC JS-002)
  Latch-up Performance Exceeds ±100mA per JEDEC JESD78F
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET74LVC1G08	SC70-5 (1.3mm × 2.1mm)	Tape and Reel, 3K/Reel	3
ET74LVC1G08T	SOT23-5 (1.6mm × 2.9mm)	Tape and Reel, 3K/Reel	3
ET74LVC1G08Y	DFN6 (1.0mm × 1.5mm)	Tape and Reel, 3K/Reel	1
ET74LVC1G08Y1	DFN4 (0.8mm × 0.8mm)	Tape and Reel, 10K/Reel	1

Pin Configuration



SC70-5/SOT23-5 DFN6 DFN4

Figure1. Pin Configuration

Pin Function

Pin No.				Pin Name	Pin Function
SC70-5	SOT23-5	DFN6	DFN4		
1	1	1	1	A	Input A
2	2	2	2	B	Input B
3	3	3	3	GND	Ground
4	4	4	4	Y	Output Y
/	/	5	/	NC	No Connect
5	5	6	5	VCC	Supply Voltage

Block Diagram

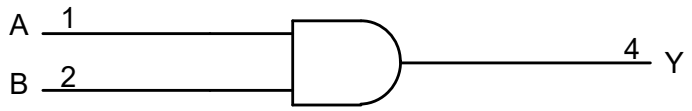


Figure2. Logic Symbol

Function Table

Input		Output
A	B	Y
L	L	L
L	H	L
H	L	L
H	H	H

Absolute Maximum Ratings

Symbol	Parameter		Value	Unit
V_{CC}	DC Supply Voltage		-0.5 to 6.5	V
V_I	DC Input Voltage (1)		-0.5 ≤ V_I ≤ 6.5	V
V_O	DC Output Voltage Output in Higher or Low State		-0.5 to V_{CC} + 0.5	V
I_{IK}	DC Input Diode Current, V_I < GND		-50	mA
I_{OK}	DC Output Diode Current, V_O < GND, V_O > V_{CC}		±50	mA
I_O	DC Output Sink Current		±50	mA
I_{CC}	DC Supply Current Per Supply Pin		100	mA
I_{GND}	DC Ground Current Per Supply Pin		-100	mA
T_{STG}	Storage Temperature Range		-65 to 150	°C
T_L	Lead Temperature, Soldering 10 Seconds		260	°C
T_J	Max Junction Temperature		150	°C
V_{ESD}	ESD Classification	Human Body Model (2)	±4000	V
		Charged Device Model (3)	±1000	
I_{LU}	Max Latch Up Current Above V_{CC} and GND at 125°C (4)		±100	mA

Stresses exceeding those listed in this table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: I_O absolute maximum rating must be observed.

Note2: HBM tested per JEDEC JS-001;

Note3: CDM tested per JEDEC JS-002;

Note4: Latch Up Current Maximum Rating tested per JEDEC JESD78F.

Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
$R_{\theta JA}$	SC70-5	Thermal Characteristics, Thermal Resistance, Junction-to-Air	300	°C/W
	SOT23-5		250	
	DFN6		440	
	DFN4		285	
P_D	SC70-5	Power Dissipation in Still Air at 85°C	215	mW
	SOT23-5		260	
	DFN6		150	
	DFN4		350	

Recommended Operating Conditions

Symbol	Parameter		Min	Max	Unit
V_{CC}	DC Supply Voltage Operating		1.65	5.5	V
	Date Retention Voltage Operating		1.5	5.5	
V_I	DC Input Voltage		0	V_{CC}	V
V_O	DC Output Voltage (High or Low State)		0	V_{CC}	V
T_A	Operating Temperature Range		-40	125	°C
t_r, t_f	Input Rise and Fall Time	V_{CC} = 2.5V ± 0.2V	0	20	ns/V
		V_{CC} = 3.0V ± 0.3V	0	10	
		V_{CC} = 5.0V ± 0.5V	0	5	

Functional operation above the stresses listed in the Recommended Operating Ranges is not implied.

Electrical Characteristics

DC Electrical Characteristics

Symbol	Parameter	Condition	V_{CC}(V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
V_{IH}	High-Level Input Voltage		1.65~1.95	0.75V_{CC}			0.75V_{CC}		V
			2.3~5.5	0.7V_{CC}			0.7V_{CC}		
V_{IL}	Low-Level Input Voltage		1.65~1.95			0.25V_{CC}		0.25V_{CC}	V
			2.3~5.5			0.3V_{CC}		0.3V_{CC}	
V_{OH}	High-Level Output Voltage	I_{OH} = -100μA	1.65~5.5	V_{CC} - 0.1	V_{CC}		V_{CC} - 0.1		V
		I_{OH} = -3mA	1.65	1.29	1.52		1.29		
		I_{OH} = -8mA	2.3	1.9	2.1		1.9		
		I_{OH} = -12mA	2.7	2.2	2.4		2.2		
		I_{OH} = -16mA	3.0	2.4	2.7		2.4		
		I_{OH} = -24mA	3.0	2.3	2.5		2.3		
		I_{OH} = -32mA	4.5	3.8	4.0		3.8		
V_{OL}	Low-Level Output Voltage	I_{OL} = 100μA	1.65~5.5		0.0	0.1		0.1	V
		I_{OL} = 3mA	1.65		0.08	0.24		0.24	
		I_{OL} = 8mA	2.3		0.20	0.3		0.3	
		I_{OL} = 12mA	2.7		0.22	0.4		0.4	
		I_{OL} = 16mA	3.0		0.28	0.4		0.4	
		I_{OL} = 24mA	3.0		0.38	0.55		0.55	
		I_{OL} = 32mA	4.5		0.42	0.55		0.55	

DC Electrical Characteristics (Continued)

Symbol	Parameter	Condition	V_{CC}(V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
I_{IN}	Input Leakage Current	V_I = 5.5V or GND	0~5.5			± 0.1		±1.0	μA
I_{OFF}	Power Off Leakage Current	V_I = 5.5V or V_O = 5.5V	0			1.0		10	μA
I_{CC}	Quiescent Supply Current	V_I = 5.5V or GND	5.5			1.0		10	μA

AC Electrical Characteristics

$t_r = t_f$ = 3ns

Symbol	Parameter	Condition	V_{CC}(V)	T_A = 25°C			-40°C ≤ T_A ≤ 125°C		Unit
				Min	Typ	Max	Min	Max	
t_{PLH} t_{PHL}	Propagation Delay (Figure3 & 4)	R_L = 1MΩ C_L = 15pF	1.65	2.0	12.5	15	2.0	16.5	ns
			1.8	2.0	10.2	12.6	2.0	15	
		R_L = 1MΩ C_L = 15pF	2.5	0.2	6.0	7.7	0.8	10.8	
		R_L = 1MΩ C_L = 15pF	3.3	0.8	5.0	6.5	0.5	9.6	
		R_L = 500Ω C_L = 50pF		1.2	5.6	7.1	1.5	10.2	
		R_L = 1MΩ C_L = 15pF	5.0	0.5	4.4	5.6	0.5	6.1	
		R_L = 500Ω C_L = 50pF		0.8	4.8	6.1	0.8	6.6	

Capacitance Characteristics

Symbol	Parameter	Condition	Typ	Unit
C_{IN}	Input Capacitance	V_{CC} = 5.5V, V_I = 0V or V_{CC}	3.5	pF
C_{PD}	Power Dissipation Capacitance [(5)]	10MHz, V_{CC} = 3.3V, V_I = 0V or V_{CC}	26	pF
		10MHz, V_{CC} = 5.5V, V_I = 0V or V_{CC}	30	

Note5. C_{PD} is used to determine the dynamic power dissipation (P_D in μW).

$P_D = C_{PD} \times V_{CC}^2 \times f_i \times N + \Sigma(C_L \times V_{CC}^2 \times f_o)$ where:

f_i = input frequency in MHz;

f_o = output frequency in MHz;

C_L = output load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching;

$\Sigma(C_L \times V_{CC}^2 \times f_o)$ = sum of outputs.

AC Test Circuit

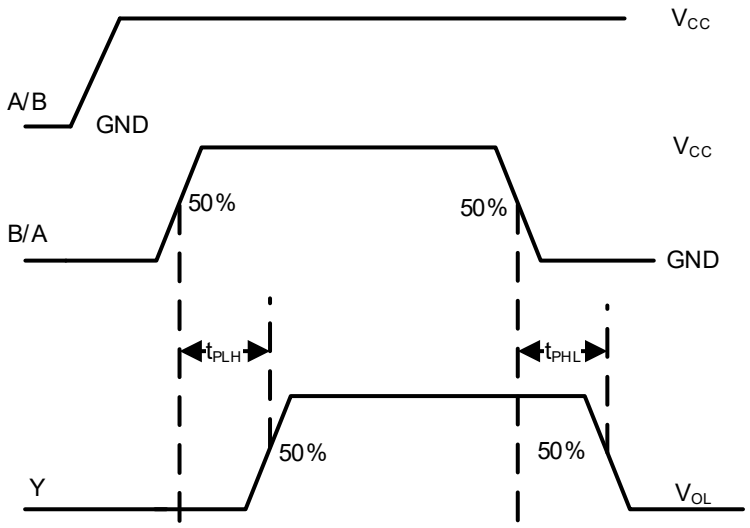


Figure3. Switching Waveform

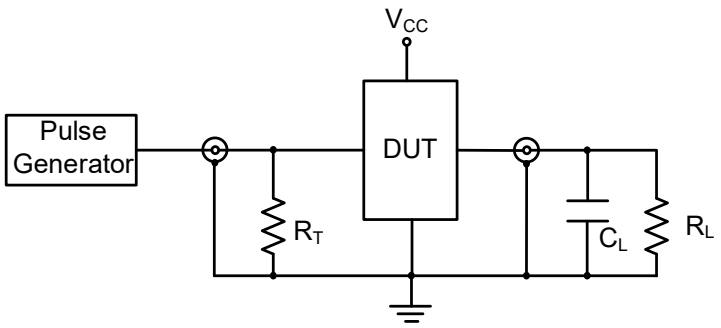
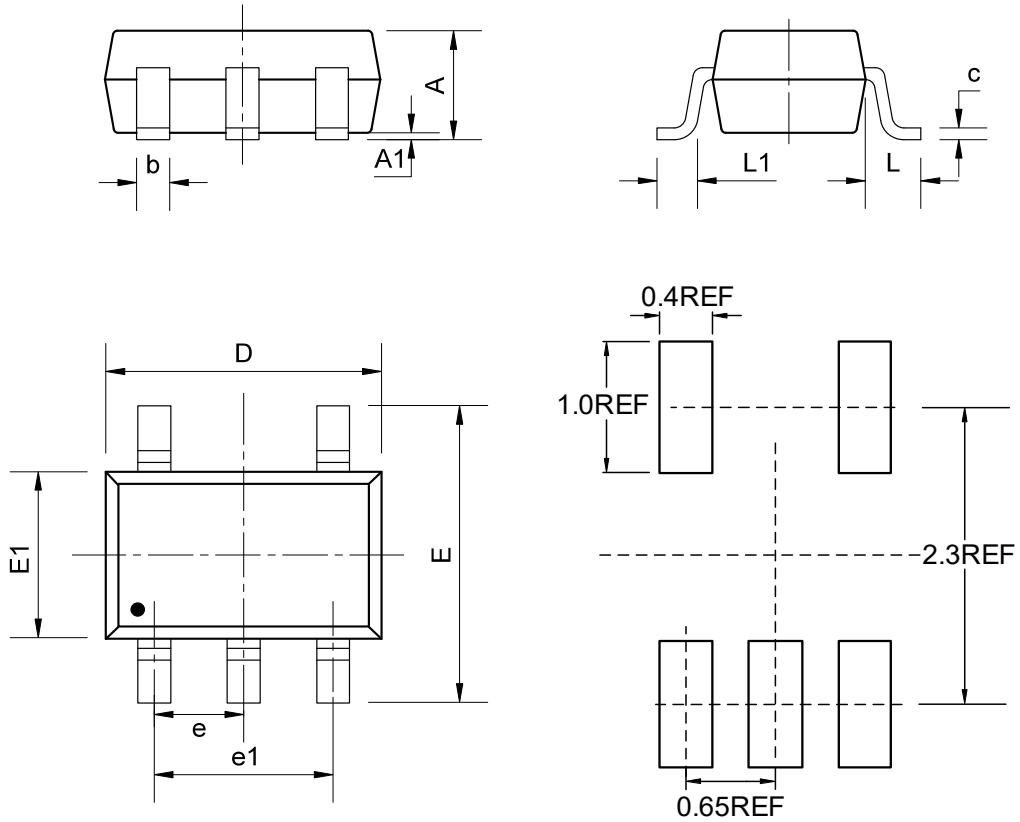


Figure4. Test Circuit

Package Dimension

SC70-5 (1.3mm × 2.1mm)

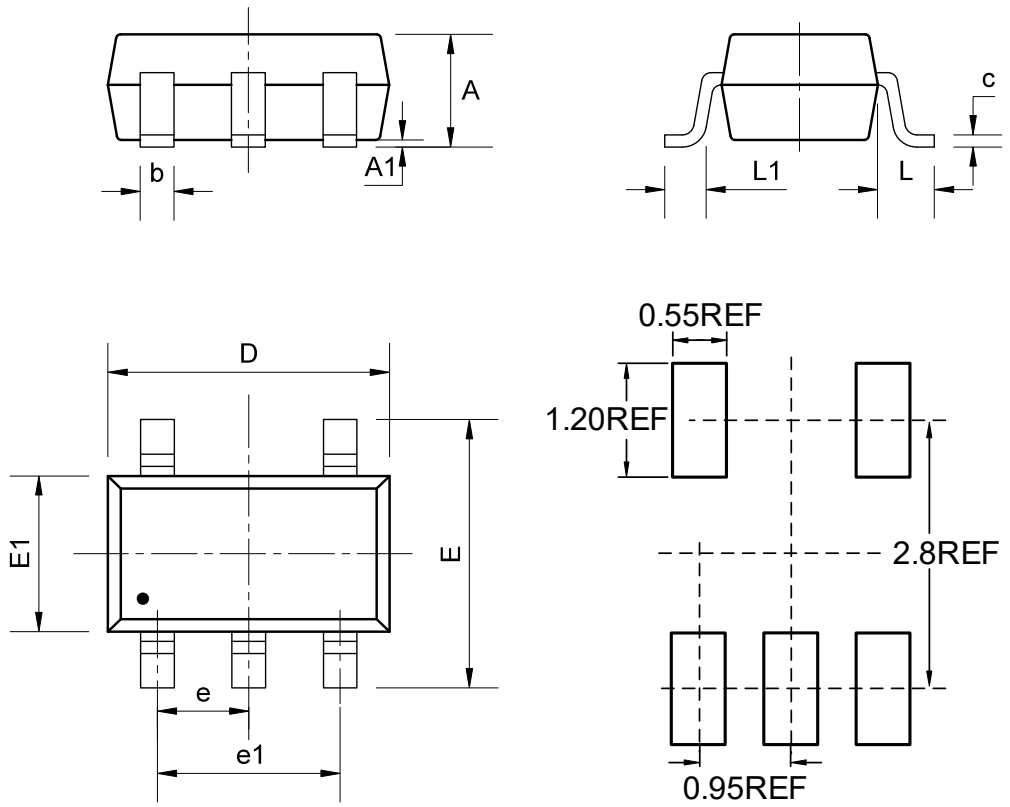


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.10
A1	0.00	-	0.15
b	0.15	-	0.35
c	0.08	-	0.20
D	2.00	2.10	2.30
e	0.65BSC		
e1	1.30BSC		
E	2.15	2.30	2.50
E1	1.15	1.30	1.45
L	0.50REF		
L1	0.33REF		

SOT23-5 (1.6mm × 2.9mm)

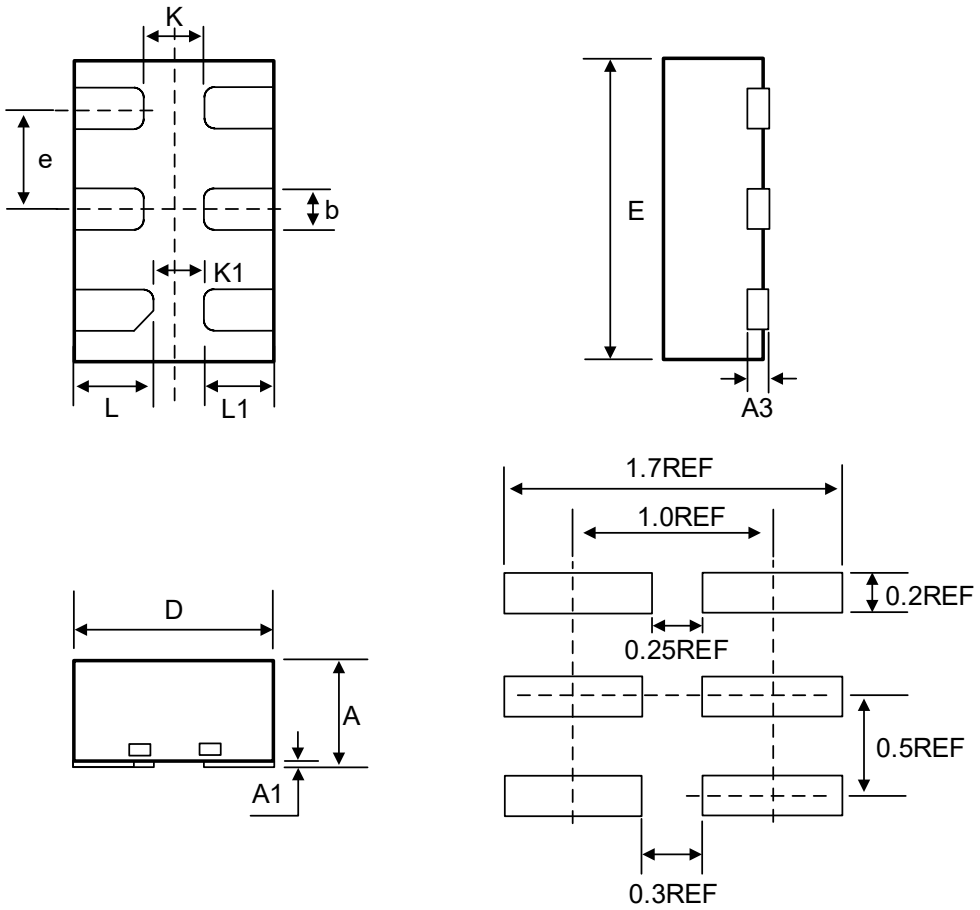


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	-	-	1.45
A1	0.00	-	0.15
b	0.28	0.35	0.50
c	0.08	0.15	0.22
D	2.75	2.9	3.05
e	0.90	0.95	1.00
e1	1.80	1.90	2.00
E	2.60	2.80	3.00
E1	1.45	1.6	1.75
L	0.60REF		
L1	0.30	0.45	0.60

DFN6 (1.0mm × 1.5mm)

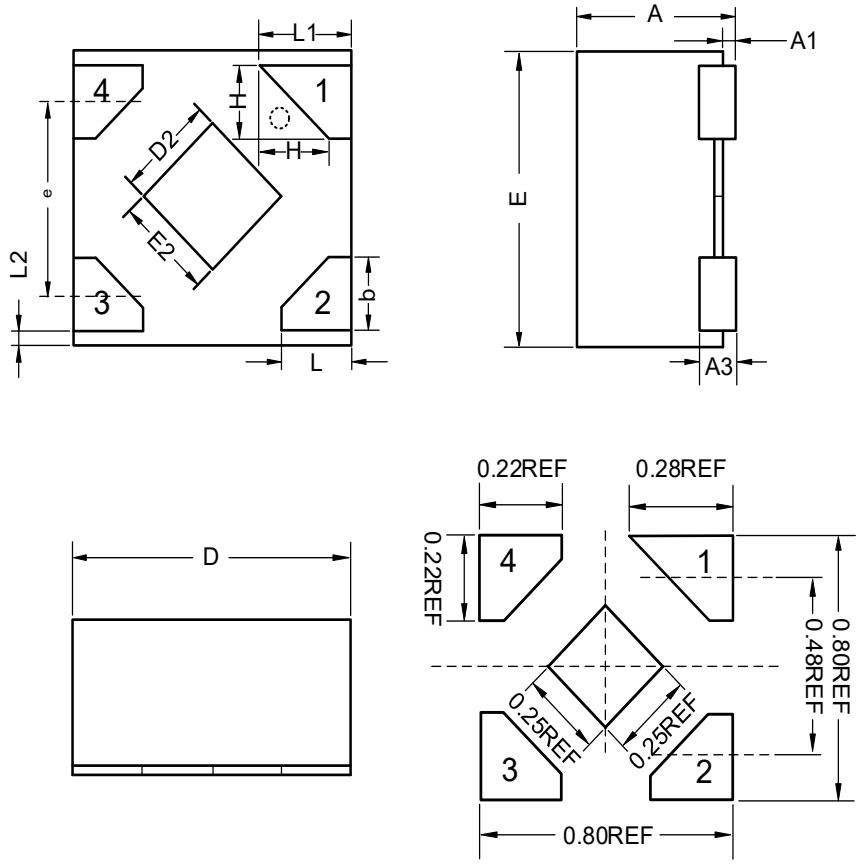


COMMON DIMENSIONS

(Unit: mm)

SYMBOL	MIN	NOM	MAX
A	0.50	--	0.60
A1	0.00	0.02	0.05
A3		0.10REF	
b	0.15	0.20	0.25
D	0.90	1.00	1.10
E	1.40	1.50	1.60
e		0.50BSC	
K		0.30REF	
K1		0.25REF	
L	0.35	0.40	0.45
L1	0.30	0.35	0.40

DFN4 (0.8mm × 0.8mm)



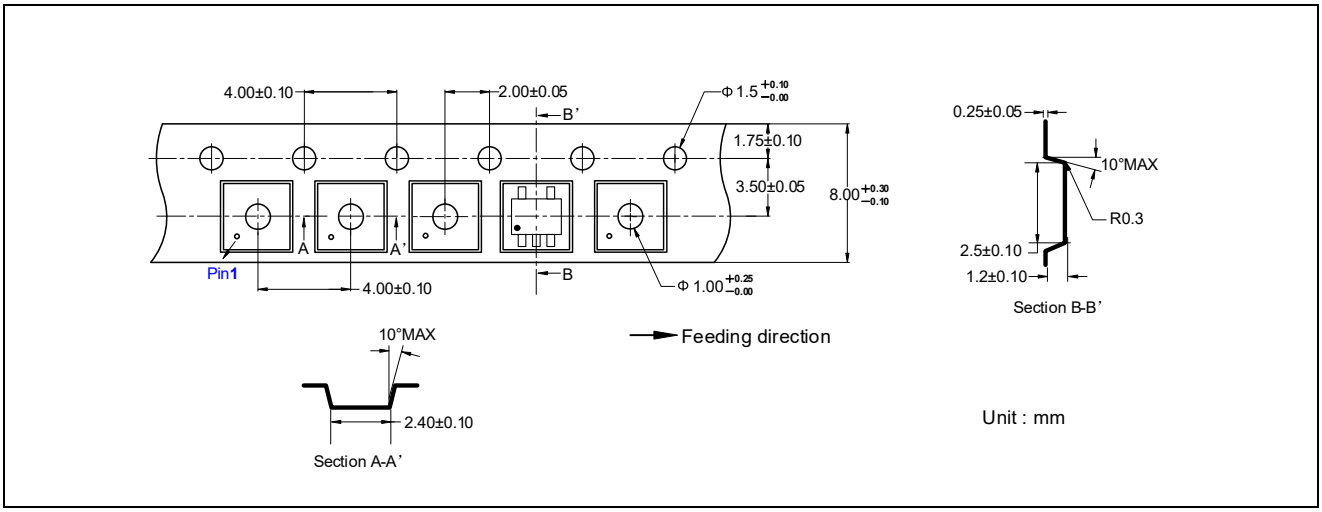
COMMON DIMENSIONS

(Unit: mm)

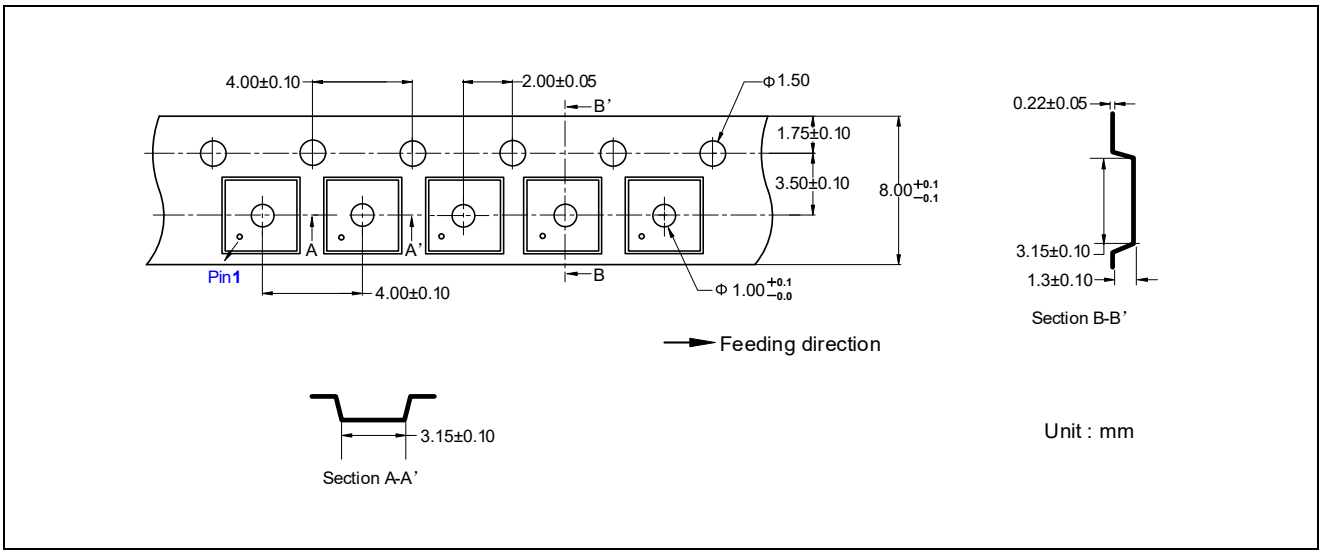
SYMBOL	MIN	NOM	MAX
A	0.34	0.37	0.40
A1	0.00	0.02	0.05
A3		0.10REF	
b	0.17	0.22	0.27
D	0.75	0.80	0.85
E	0.75	0.80	0.85
D2	0.20	0.25	0.30
E2	0.20	0.25	0.30
e	0.43	0.48	0.53
H		0.22REF	
L	0.17	0.22	0.27
L1	0.18	0.28	0.38
L2		0.05REF	

Tape Information

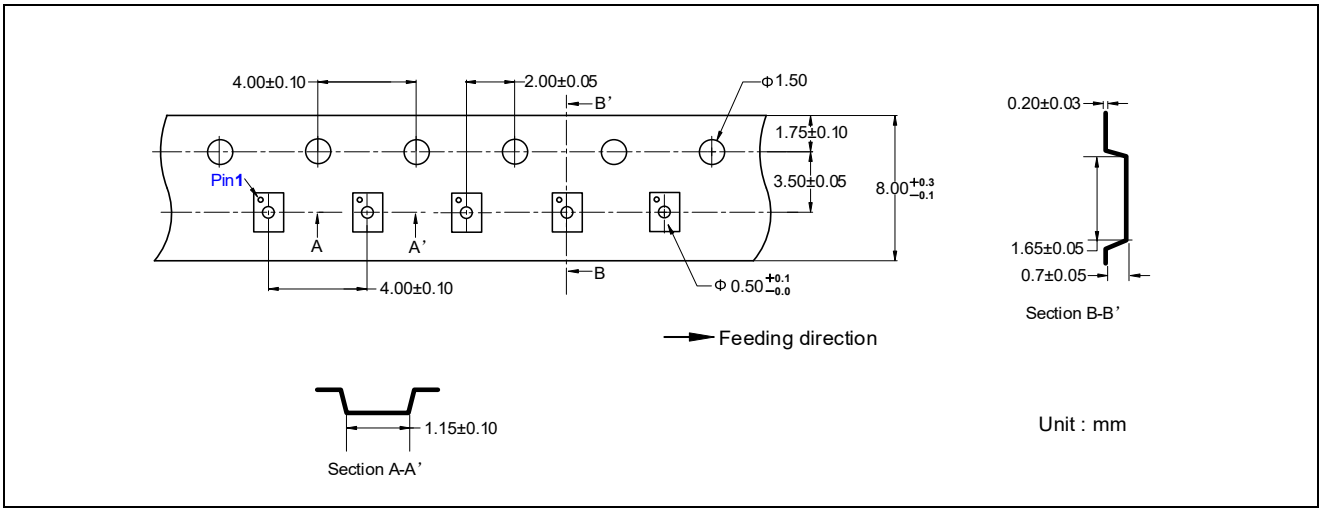
SC70-5 (1.3mm × 2.1mm)



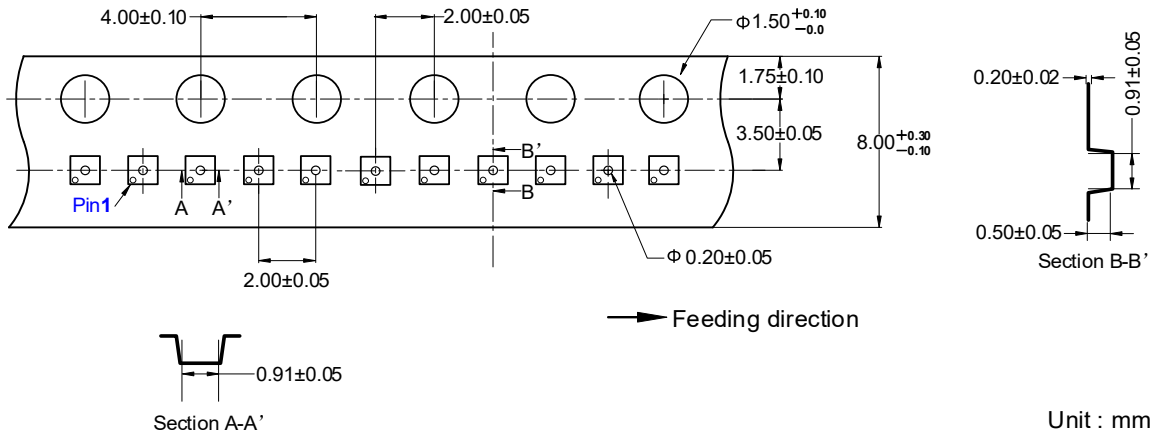
SOT23-5 (1.6mm × 2.9mm)



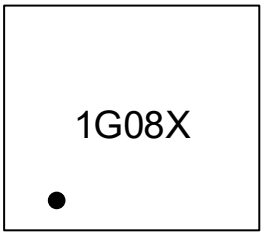
DFN6 (1.0mm × 1.5mm)



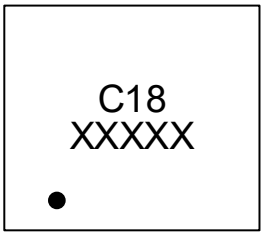
DFN4 (0.8mm × 0.8mm)



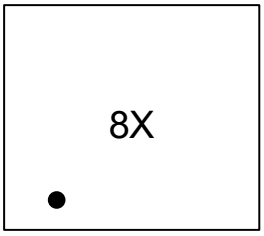
Marking Information



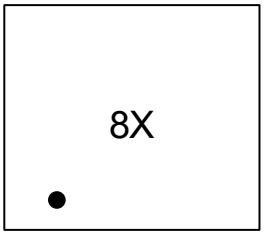
ET74LVC1G08
1G08 = Part Number
X = Tracking Number



ET74LVC1G08T
C18 = Part Number
XXXXX = Tracking Number



ET74LVC1G08Y
8 = Part Number
X = Tracking Number



ET74LVC1G08Y1
8 = Part Number
X = Tracking Number

Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
1.0	2017-07-18	Original Version	Ma yongjian	Ma yongjian	Liu jiaying
1.1	2019-07-18	Update AC Table and Device Information	Ma yongjian	Ma yongjian	Liu jiaying
1.2	2021-06-25	Update Cpd in AC table and Iin/Ioff/Icc in DC Table	Ma yongjian	Ma yongjian	Liu jiaying
1.3	2022-01-18	Add SC70-5 Recommended Pad Layout	Shi bo	Shi bo	Zhu junli
1.4	2022-04-26	ESD Update	Shi bo	Shi bo	Zhu junli
1.5	2022-07-07	Add θ_{JB}, T_A = 125°C	Shi bo	Shi bo	Zhu junli
1.6	2022-10-14	Update Format and Thermal Characteristics	Wu han	Shi bo	Zhu junli
1.7	2023-11-29	Update Typeset / Package Picture	Shi bo	Shi bo	Liu jiaying
1.8	2025-05-29	Add Packing Option	Yang xiaoxu	Yang xiaoxu	Liu jiaying
1.9	2025-07-21	Update Electrical Characteristics	Yu yifan	Yang xiaoxu	Liu jiaying
1.9.a	2025-09-02	Update Typeset	Peng junjie	Yang xiaoxu	Liu jiaying
1.10	2025-12-03	Add Marking and Tape Information	Yang xiaoxu	Yang xiaoxu	Liu jiaying
1.11	2026-01-23	Delete θ_{JB}, Add DFN4 Package, Update Format	Xu tao	Yang xiaoxu	Liu jiaying