

ET5H5XX - High Input Very-Low IQ 300mA LDO

General Description

ET5H5XX series are the high input very low I_Q 300mA LDO with enable function that operates output from 1.8V、3.0V~13V, is designed specifically for portable battery-powered applications which require ultra-low quiescent current. The very-low consumption of type 2.8 μ A ensures long battery life and dynamic transient boost feature improves device transient response for wireless communication applications.

ET5H5XX series are offered SOT89-3, SOT89-5, SOT23-5, SOT23-3, DFN4(1 \times 1) packages.

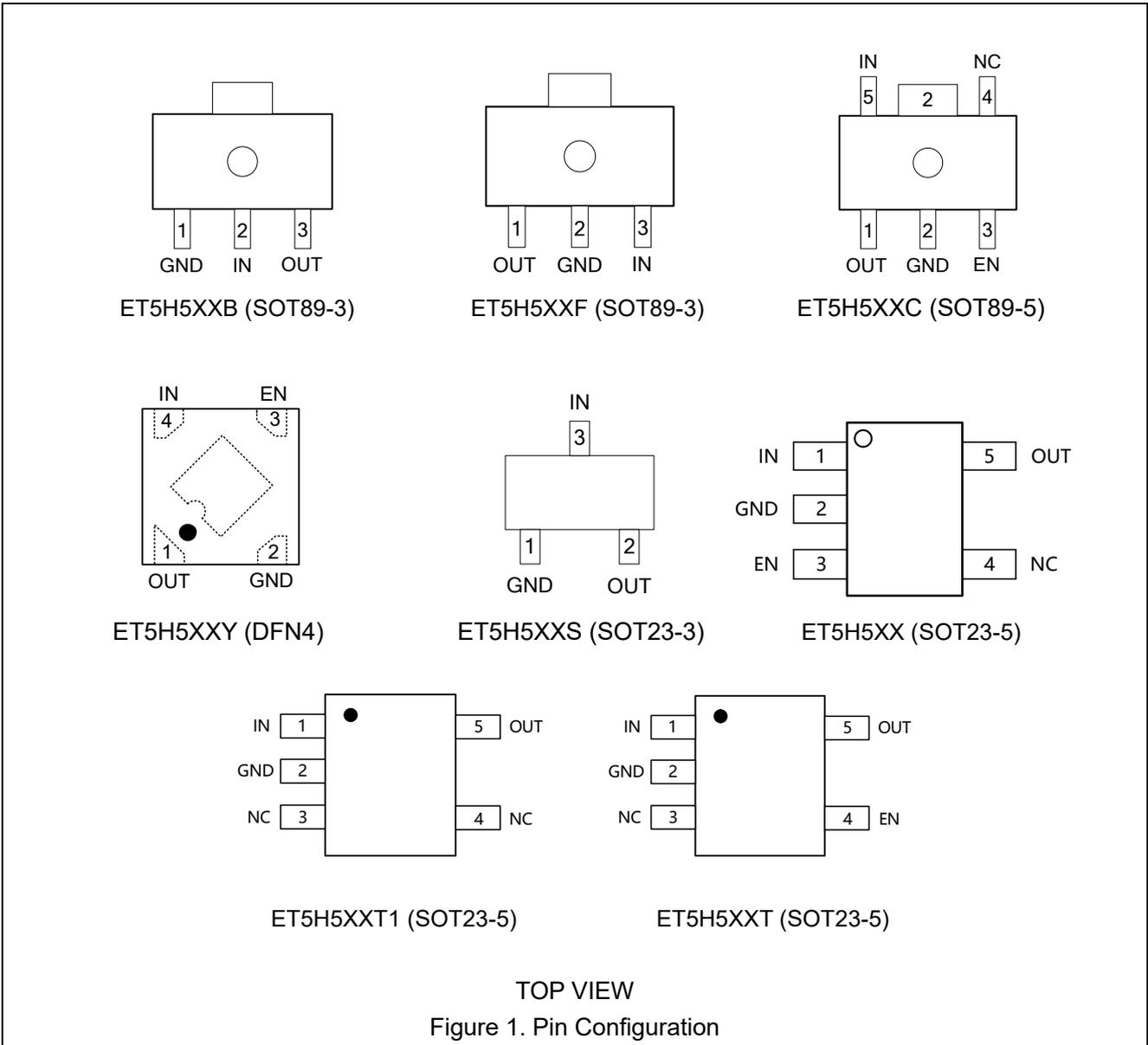
Features

- Wide Input Voltage Range: 3.0V to 40V
- Up to 300mA Load Current
- Very Low I_Q : 2.8 μ A
- Fixed Output Voltage: 1.8V or 3.0V~13V@50mV/Step
- Low Dropout: 1000mV Typical @ 300mA / $V_{OUT}=5.0V$
- Excellent Load/Line Transient Response
- High Ripple Rejection: 52dB Typical at 1KHz
- Part No. and Package Information

Part No.	Package	Packing Option	MSL
ET5H5XXB	SOT89-3	Tape and Reel, 1K/Reel	3
ET5H5XXF		Tape and Reel, 1K/Reel	3
ET5H5XXC	SOT89-5	Tape and Reel, 1K/Reel	3
ET5H5XXS	SOT23-3	Tape and Reel, 3K/Reel	3
ET5H5XXT1	SOT23-5	Tape and Reel, 3K/Reel	3
ET5H5XXT		Tape and Reel, 3K/Reel	3
ET5H5XX	SOT23-5 (Default)	Tape and Reel, 3K/Reel	3
ET5H5XXY	DFN4	Tape and Reel, 10K/Reel	1

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Pin Configuration



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Pin Function

Pin No.								Pin Name	Pin Function
SOT89-3		SOT89-5	SOT23-3	DFN4	SOT23-5				
XXB	XXF	XXC	XXS	XXY	XX	XXT1	XXT		
1	2	2	1	2	2	2	2	GND	Ground.
2	3	5	3	4	1	1	1	IN	Supply Input Pin.
3	1	1	2	1	5	5	5	OUT	Output Pin.
		3		3	3		4	EN	Enable Control Input, Active High.
		4			4	3, 4	3	NC	No Connection.

Mark Specification Label

Part No.	Marking								V _{OUT}
	SOT89-3		SOT89-5	SOT23-3	DFN4	SOT23-5			
	XXB	XXF	XXC	XXS	XXY	XX	XXT1	XXT	
ET5H518	18B	18F	18C	18S	XX	18	18T1	18T	1.8V
ET5H530	30B	30F	30C	30S	XX	30	30T1	30T	3.0V
ET5H533	33B	33F	33C	33S	XX	33	33T1	33T	3.3V
ET5H536	36B	36F	36C	36S	XX	36	36T1	36T	3.6V
ET5H540	40B	40F	40C	40S	XX	40	40T1	40T	4.0V
ET5H550	50B	50F	50C	50S	XX	50	50T1	50T	5.0V
ET5H553	53B	53F	53C	53S	XX	53	53T1	53T	5.3V
ET5H560	60B	60F	60C	60S	XX	60	60T1	60T	6.0V
ET5H570	70B	70F	70C	70S	XX	70	70T1	70T	7.0V
ET5H580	80B	80F	80C	80S	XX	80	80T1	80T	8.0V
ET5H590	90B	90F	90C	90S	XX	90	90T1	90T	9.0V
ET5H5120	120B	120F	120C	120S	XX	120	120T1	120T	12.0V

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Block Diagram

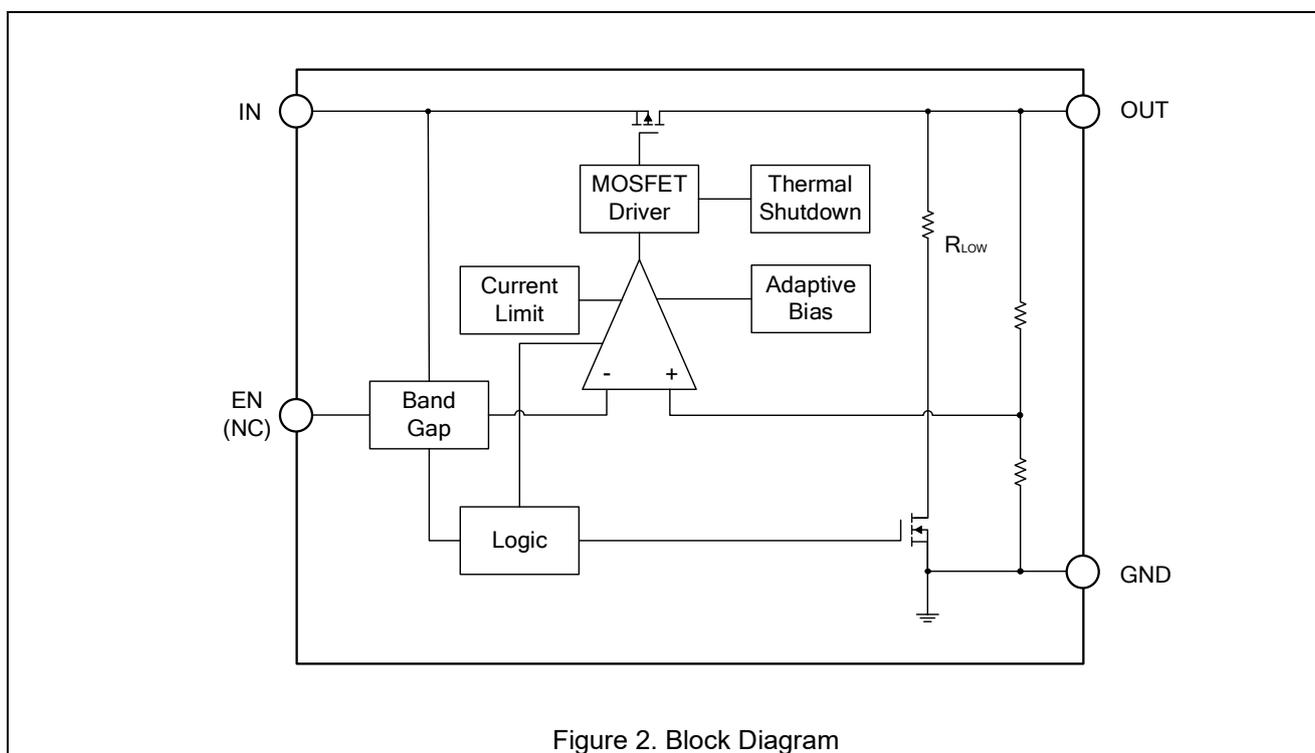


Figure 2. Block Diagram

Functional Description

Input Capacitor

A 0.47 μ F~10 μ F ceramic capacitor is recommended to connect between IN and GND pins to decouple input power supply glitch and noise. The amount of the capacitance may be increased without limit. This input capacitor must be located as close as possible to the device to assure input stability and less noise. For PCB layout, a wide copper trace is required for both IN and GND.

Output Capacitor

An output capacitor is required for the stability of the LDO. The recommended output capacitance is from 0.47 μ F to 10 μ F, Equivalent Series Resistance (ESR) is from 5m Ω to 100m Ω , and temperature characteristics are X7R or X5R. Higher capacitance values help to improve load/line transient response. The output capacitance may be increased to keep low undershoot/overshoot. Place output capacitor as close as possible to OUT and GND pins.

Enable (If EN Pin Exist)

The ET5H5XX delivers the output power when it is set to enable state. When it works in disable state, there is no output power and the operation quiescent current is almost zero. The enable pin (EN) is active high.

Dropout Voltage

The ET5H5XX uses a PMOS pass transistor to achieve low dropout. When ($V_{IN} - V_{OUT}$) is less than the dropout voltage (V_{DROPO}), the PMOS pass device is in the linear region of operation and the input-to-output resistance is the $R_{DS(ON)}$ of the PMOS pass element. V_{DROPO} scales approximately with output current because the PMOS device behaves like a resistor in dropout mode. As with any linear regulator, PSRR and transient response degrade as ($V_{IN} - V_{OUT}$) approaches dropout operation.

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Thermal Shutdown

Thermal shutdown protection disables the output when the junction temperature rises to approximately 155°C. Disabling the device eliminates the power dissipated by the device, allowing the device to cool. When the junction temperature cools to approximately 125°C, the output circuitry is again enabled. Depending on power dissipation, thermal resistance, and ambient temperature, the thermal protection circuit may cycle on and off. This cycling limits regulator dissipation, protecting the LDO from damage as a result of overheating. Activating the thermal shutdown feature usually indicates excessive power dissipation as a result of the product of the ($V_{IN} - V_{OUT}$) voltage and the load current. For reliable operation, limit junction temperature to 125°C maximum.

Thermal Considerations

For continuous operation, do not exceed absolute maximum junction temperature. The maximum power dissipation depends on the thermal resistance of the IC package, PCB layout, rate of surrounding airflow, and difference between junction and ambient temperature. The maximum power dissipation can be calculated by the following formula:

$$P_{D(MAX)} = (T_{J(MAX)} - T_A) / \theta_{JA}$$

where $T_{J(MAX)}$ is the maximum junction temperature, T_A is the ambient temperature, and θ_{JA} is the junction to ambient thermal resistance. The maximum power dissipation depends on the operating ambient temperature for fixed $T_{J(MAX)}$ and thermal resistance, θ_{JA} .

Current-Limit Protection

The ET5H5XX provides current limit function to prevent the device from damages during over-load or shorted-circuit condition. This current is detected by an internal sensing transistor.

Layout Guidelines

- Place input and output capacitors as close to the device as possible.
- Use copper planes for device connections in order to optimize thermal performance.
- Place thermal vias around the device to distribute heat.

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Absolute Maximum Ratings

Symbol	Rating	Value	Unit
V_{IN}	Input Voltage ⁽¹⁾	-0.3~43	V
V_{OUT}	Output Voltage	-0.3~16	V
V_{EN}	Chip Enable Input (If EN Pin Exist)	-0.3~43	V
$T_{J(MAX)}$	Maximum Junction Temperature	150	°C
T_{STG}	Storage Temperature	-65~150	°C
$V_{ESD}^{(2)}$	HBM Capability	±2000	V
	CDM Capability	±1500	V
$I_{LU}^{(2)}$	Latch Up Current Maximum Rating	±200	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Note1: Refer to Electrical Characteristics and Application Information for Safe Operating Area.

Note2: This device series incorporates ESD protection and is tested by the following methods:

ESD Human Body Model tested per JEDEC JS-001;

CDM tested per JEDEC JS-002;

Latch up Current Maximum Rating tested per JEDEC JESD78F.

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Thermal Characteristics

Symbol	Package	Ratings	Value	Unit
R _{θJA}	SOT89-3	Thermal Characteristics, Thermal Resistance, Junction-to-Air	135	°C/W
	SOT89-5		80	
	SOT23-5		250	
	SOT23-3		360	
	DFN4		250	
P _D	SOT89-3	Power Dissipation@25°C PCB Board Dimension: 50mm x 50mm (2layer) Copper :1oz	920	mW
	SOT89-5		1500	
	SOT23-5		500	
	SOT23-3		420	
	DFN4		500	

Recommended Operating Conditions

Symbol	Item	Rating	Unit
V _{IN}	Input Voltage	3.0 to 40	V
I _{OUT}	Output Current	0 to 300	mA
T _J	Operating Junction Temperature	-40 to 125	°C
C _{IN}	Effective Input Ceramic Capacitor Value	0.47 to 10	μF
C _{OUT}	Effective Output Ceramic Capacitor Value	0.47 to 10	μF
ESR	Input and Output Capacitor Equivalent Series Resistance (ESR)	5 to 100	mΩ

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Electrical Characteristics

($V_{IN} = V_{OUT} + 2V$; $I_{OUT} = 1mA$, $C_{IN} = C_{OUT} = 1.0\mu F$, unless otherwise noted. Typical values are at $T_A = +25^\circ C$.)

Symbol	Parameter	Test Conditions	Min	Typ	Max	Unit
V_{IN}	Operating Input Voltage ⁽³⁾		3.0		40	V
V_{OUT}	Output Voltage	$I_{OUT} = 1mA, 25^\circ C$	-2%		+2%	V
		$I_{OUT} = 1mA, -40\sim 125^\circ C$	-3%		+3%	
I_Q	Quiescent Current	$I_{OUT} = 0mA, 25^\circ C$		2.8	6.5	μA
I_{Q_OFF}	Standby Current	$V_{EN} = 0V$ (If EN Pin Exist), $25^\circ C$			1	μA
Reg_{Line}	Line Regulation ⁽⁶⁾	$V_{IN} = V_{OUT} + 1V$ to $40V$, $I_{OUT} = 10mA$		40	90	mV
Reg_{LOAD}	Load Regulation ⁽⁶⁾	$1mA \leq I_{OUT} \leq 300mA$, $V_{IN} = V_{OUT} + 2V$		90	180	mV
V_{DROP}	Dropout Voltage $I_{OUT}=300mA$ ^{(4) (6)}	$V_{OUT} = 1.8V$		950	1450	mV
		$V_{OUT} = 3.0\sim 13V$		1000	1500	
I_{LMT}	Current Limit	$V_{IN} = V_{OUT} + 2V$	350	450	800	mA
I_{SHORT}	Short Current Limit	$V_{OUT} = 0V$		55	120	mA
V_{IH}	EN Pin Threshold Voltage	EN Input Voltage "H" (If EN Pin Exist)	1.4			V
V_{IL}	EN Pin Threshold Voltage	EN Input Voltage "L" (If EN Pin Exist)			0.4	V
I_{EN}	EN Pin Current	$V_{EN} = 0\sim 40V$ (If EN Pin Exist)		1		μA
PSRR	Power Supply Rejection Ratio ⁽⁵⁾	$f = 1 kHz, V_{IN} = V_{OUT} + 2V$ $I_{OUT} = 20mA$		52		dB
e_n	Output Noise Voltage ⁽⁵⁾	$V_{IN} = V_{OUT} + 2V, I_{OUT} = 1mA$, $f = 10Hz$ to $100KHz$, $V_{OUT} = 3V, C_{OUT} = 1\mu F$		30* V_{OUT}		μV_{rms}
t_{ON}	Turn-On Time	From Assertion of V_{EN} to $V_{OUT} = 90\%V_{OUT(NOM)}$		1		ms
V_{TRLN}	Line transient	$V_{IN} = V_{OUT} + 2V$ to $V_{OUT} + 10V$ in $10\mu s, I_{OUT} = 1mA, T_A = +25^\circ C$		50		mV
		$V_{IN} = V_{OUT} + 10V$ to $V_{OUT} + 2V$ in $10\mu s, I_{OUT} = 1mA, T_A = +25^\circ C$		50		mV
V_{TRLD}	Load transient	$I_{OUT} = 1mA$ to $300mA$ in $10\mu s$ $V_{IN} = V_{OUT} + 2V, T_A = +25^\circ C$		210		mV
		$I_{OUT} = 300mA$ to $1mA$ in $10\mu s$ $V_{IN} = V_{OUT} + 2V, T_A = +25^\circ C$		160		mV
$T_{SD}^{(5)}$	Thermal Shutdown Temperature	Temperature Increasing from $T_A = +25^\circ C$		155		$^\circ C$
$T_{HYS}^{(5)}$	Thermal Shutdown Hysteresis	Temperature Falling from T_{TSD}		25		$^\circ C$

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Note3: Here V_{IN} means internal circuit can work normal. If $V_{IN} < V_{OUT}$, Output voltage follows $V_{IN}(I_{OUT} = 1mA)$, circuit is safety.

Note4: V_{DROP} FT test method: test the V_{OUT} voltage at $V_{SET} + V_{DROPMAX}$ with 500mA output current.

Note5: Guaranteed by design and characterization. not a FT item.

Note6: At high temperatures, the maximum load current can be calculated according to the following formula:

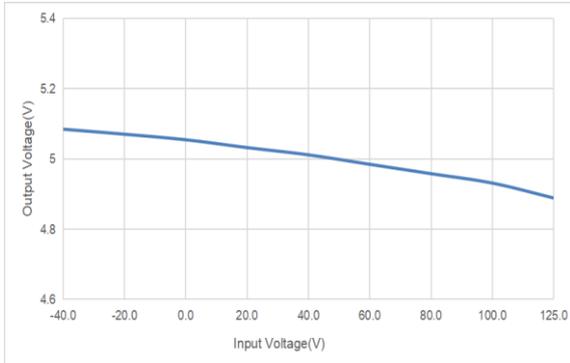
$$I_{OUT_MAX} = (T_J - T_A) / R_{\theta JA} / (V_{IN} - V_{OUT})$$

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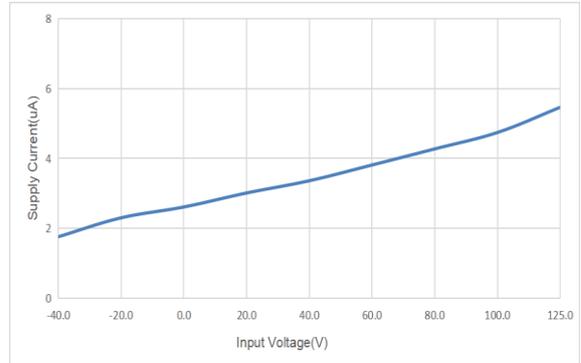
Typical Characteristics

VOLTAGE VERSION 5V

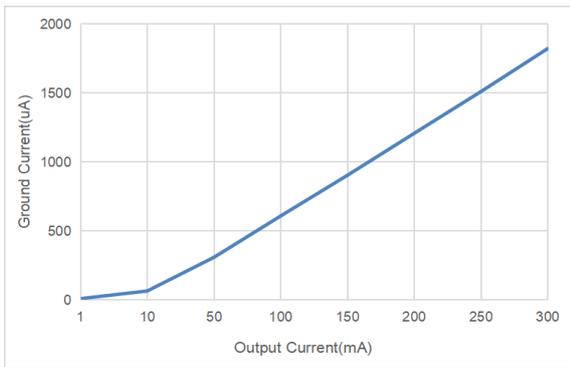
($V_{OUT} = 5V$, $V_{IN} = 7V$, $C_{IN} = C_{OUT} = 1.0\mu F$, unless otherwise noted. Typical values are at $T_A = +25^\circ C$.)



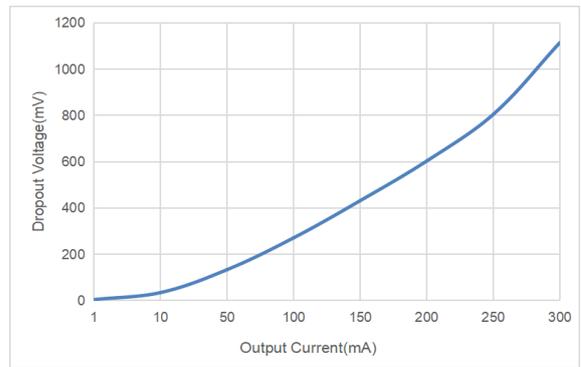
Output Voltage VS Temperature



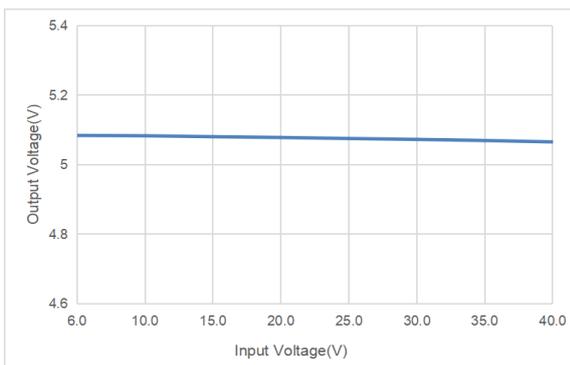
Quiescent Current VS Temperature



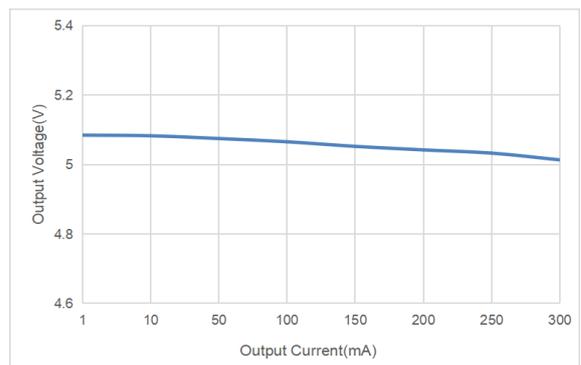
Ground Current VS Output Current



Dropout Voltage VS Output Current



Output Voltage VS Input Voltage



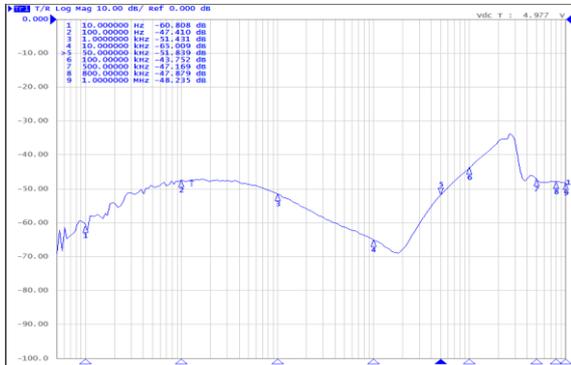
Output Voltage VS Output Current

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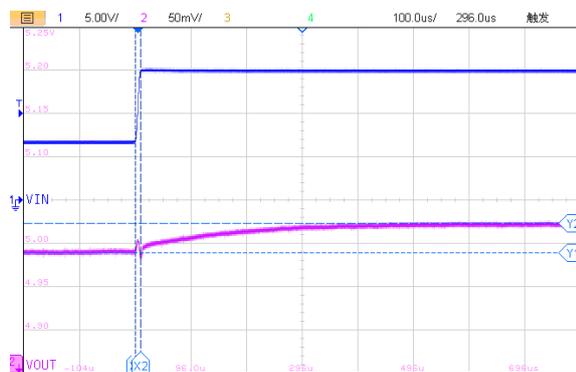
Typical Characteristics(Continued)

VOLTAGE VERSION 5V

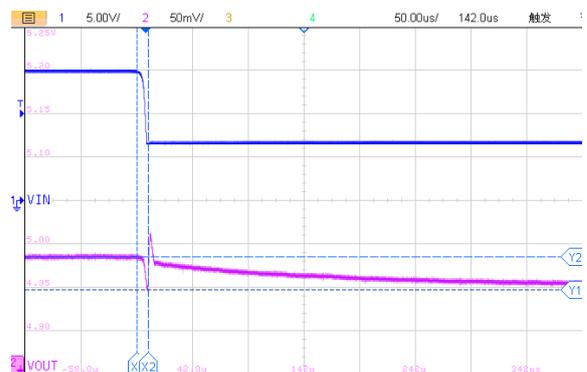
($V_{OUT} = 5V$, $V_{IN} = 7V$, $C_{IN} = C_{OUT} = 1.0\mu F$, unless otherwise noted. Typical values are at $T_A = +25^\circ C$.)



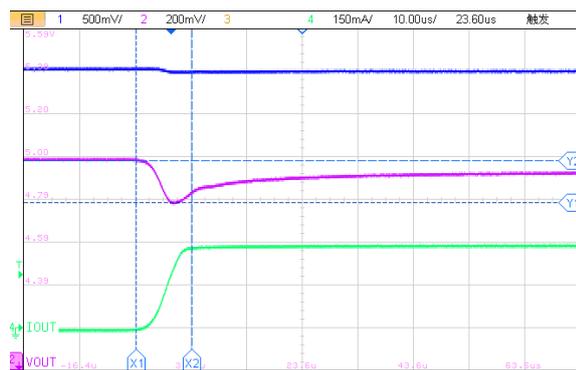
PSRR VS Output Current



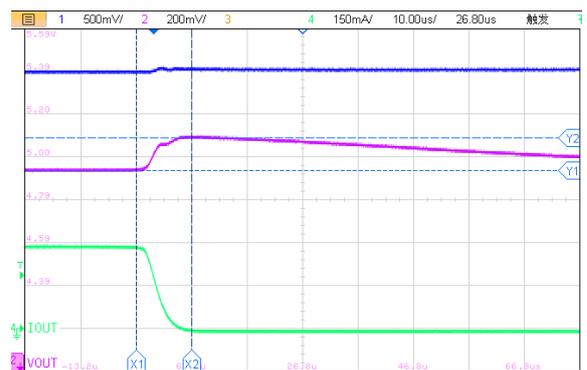
Input Transient(7~15V $t = 10\mu s$ 10mA)



Input Transient(15~7V $t = 10\mu s$ 10mA)



Load Transient(1mA~300mA $t = 10\mu s$)



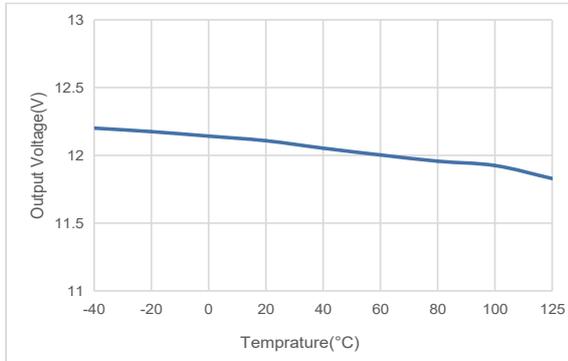
Load Transient(300mA~1mA $t = 10\mu s$)

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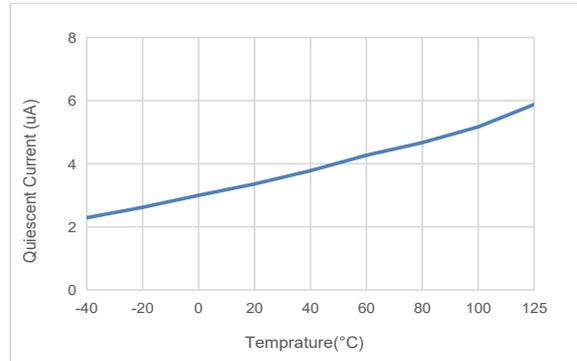
Typical Characteristics(Continued)

VOLTAGE VERSION 12V

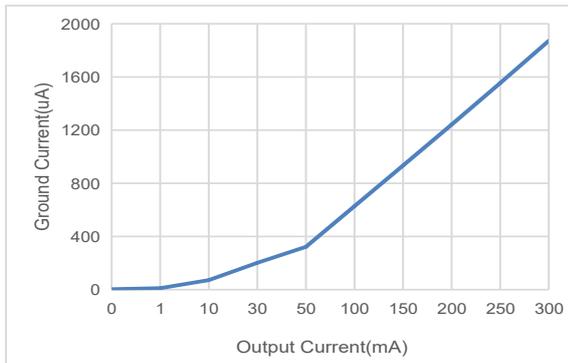
($V_{OUT} = 12V$, $V_{IN} = 14V$, $C_{IN} = C_{OUT} = 1.0\mu F$, unless otherwise noted. Typical values are at $T_A = +25^\circ C$.)



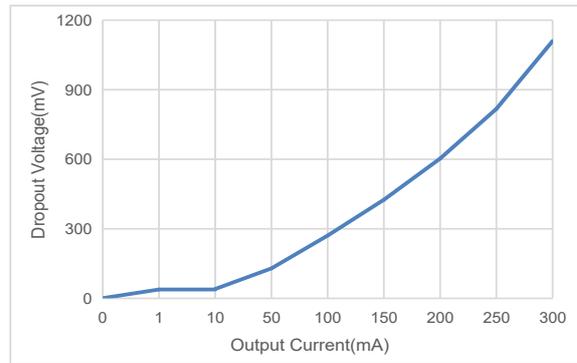
Output Voltage VS Temperature



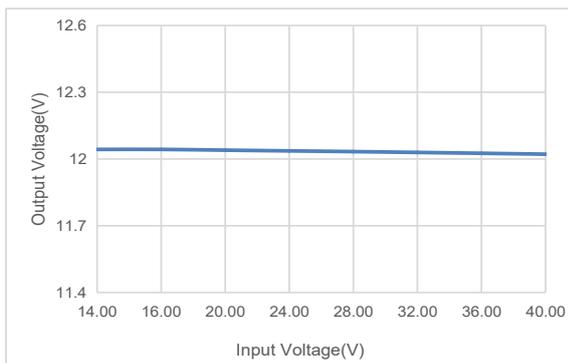
Quiescent Current VS Temperature



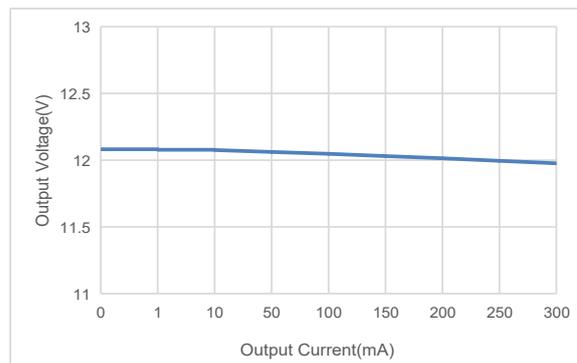
Ground Current VS Output Current



Dropout Voltage VS Output Current



Output Voltage VS Input Voltage



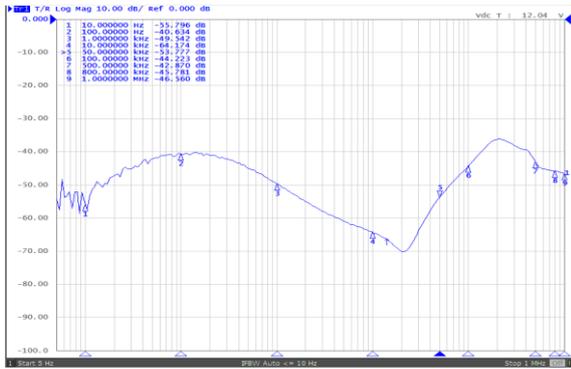
Output Voltage VS Output Current

ET5H5XX

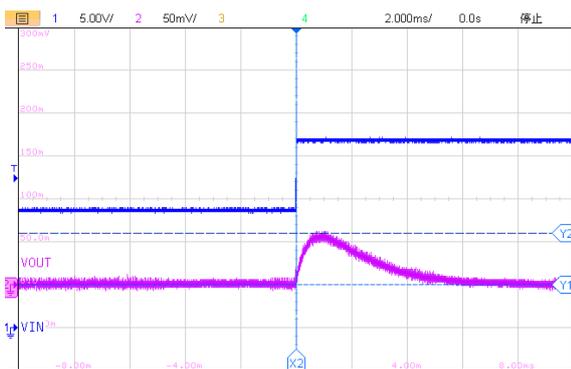
Typical Characteristics(Continued)

VOLTAGE VERSION 12V

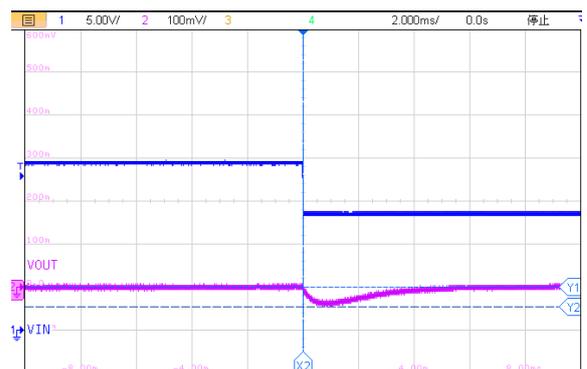
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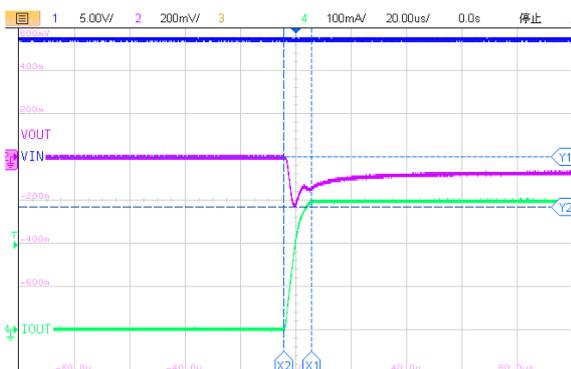
PSRR VS Output Current



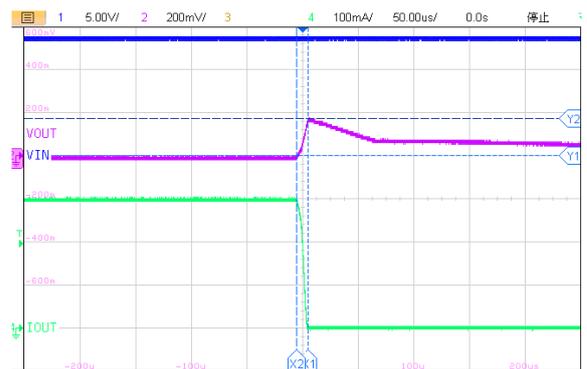
Input Transient(14~22V $t = 10\mu s$ 10mA)



Input Transient(22~14V $t = 10\mu s$ 10mA)



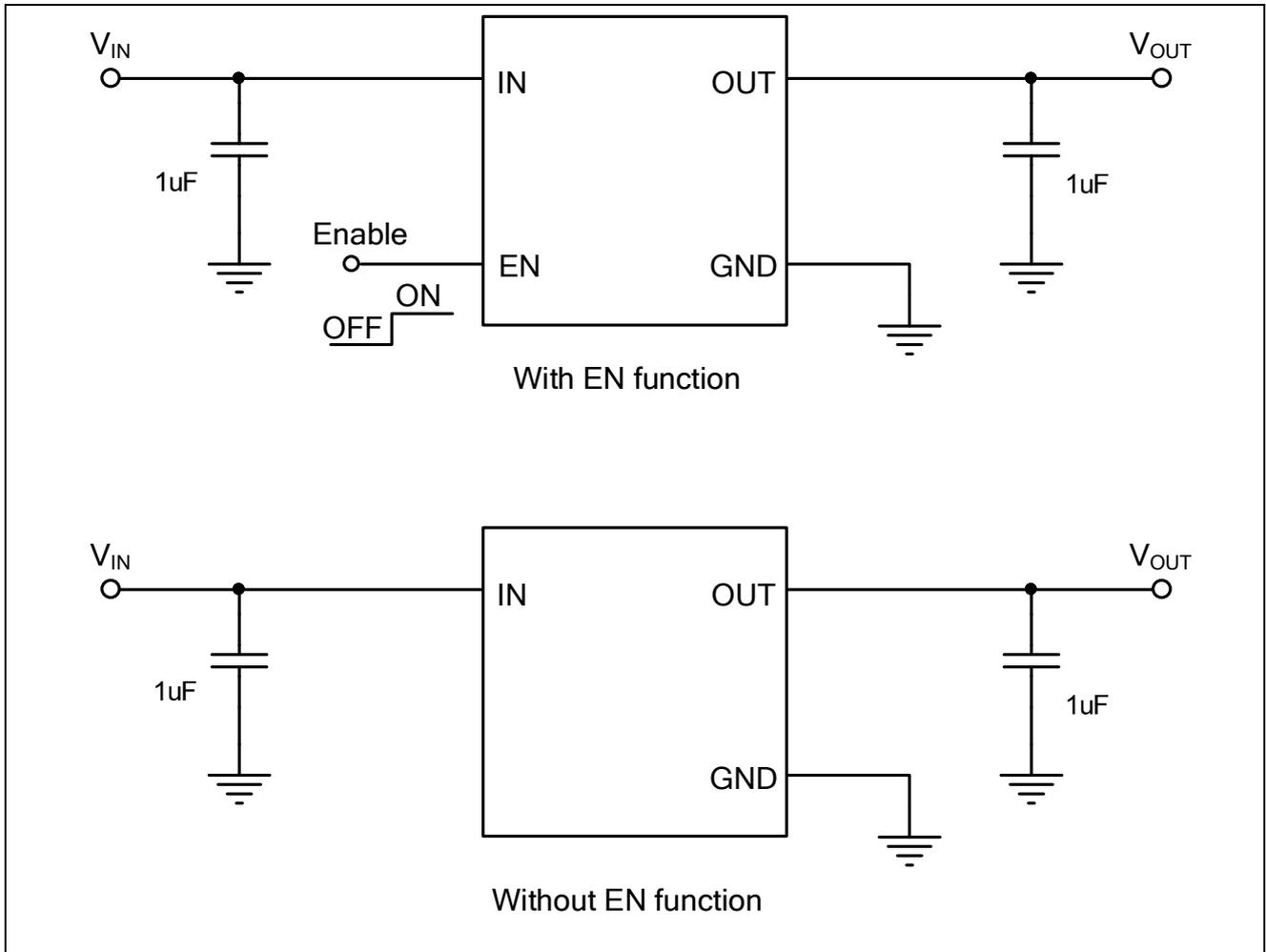
Load Transient(1mA~300mA $t = 10\mu s$)



Load Transient(300mA~1mA $t = 10\mu s$)

ET5H5XX

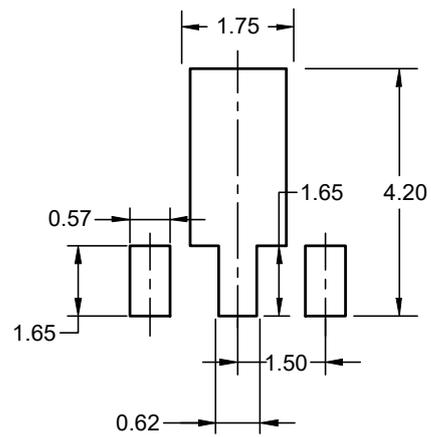
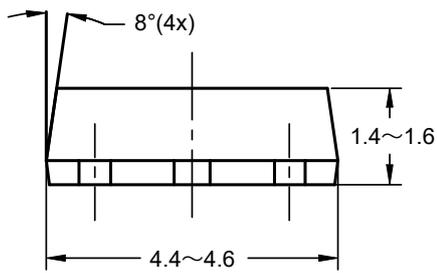
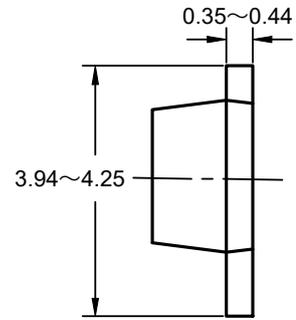
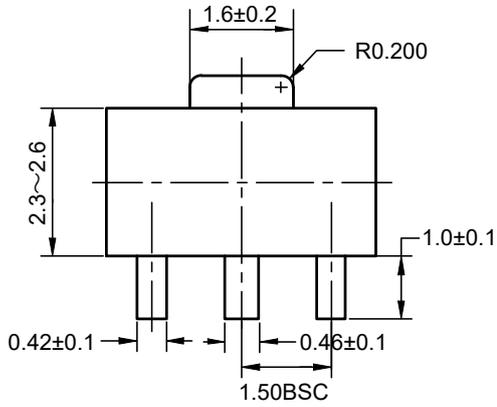
Application Circuits



ET5H5XX

Package Dimension

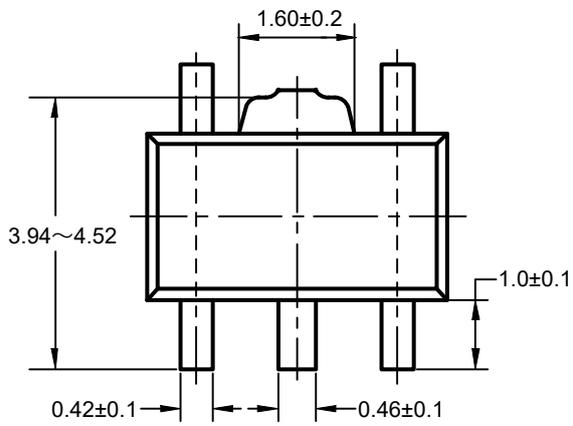
SOT89-3



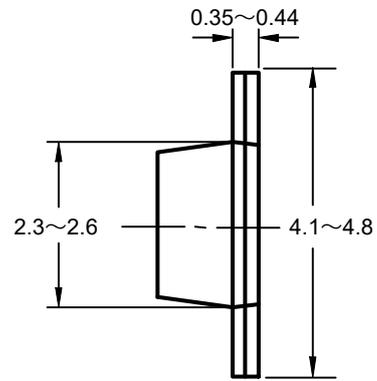
Unit: mm

ET5H5XX

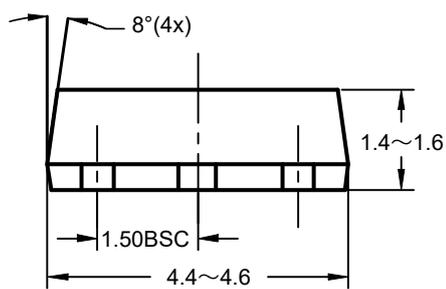
SOT89-5



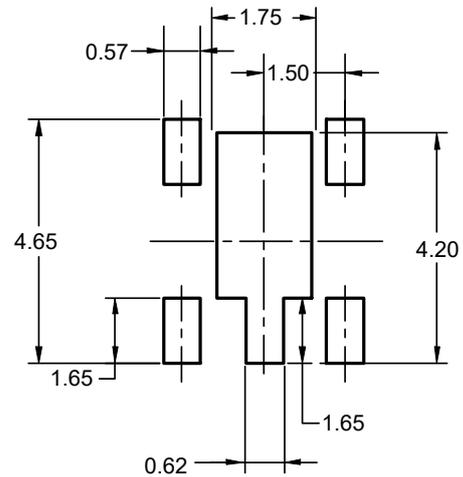
Top View



Side View



Side View

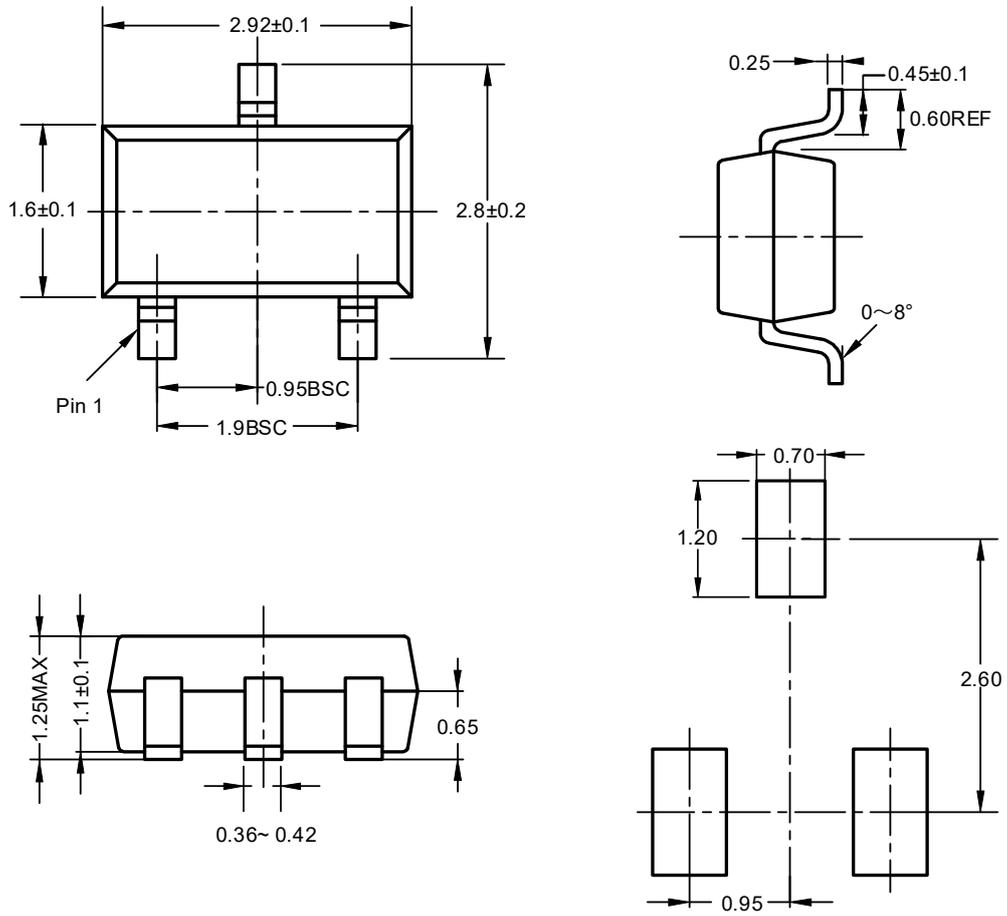


Recommended Land Pattern

Unit: mm

ET5H5XX

SOT23-3

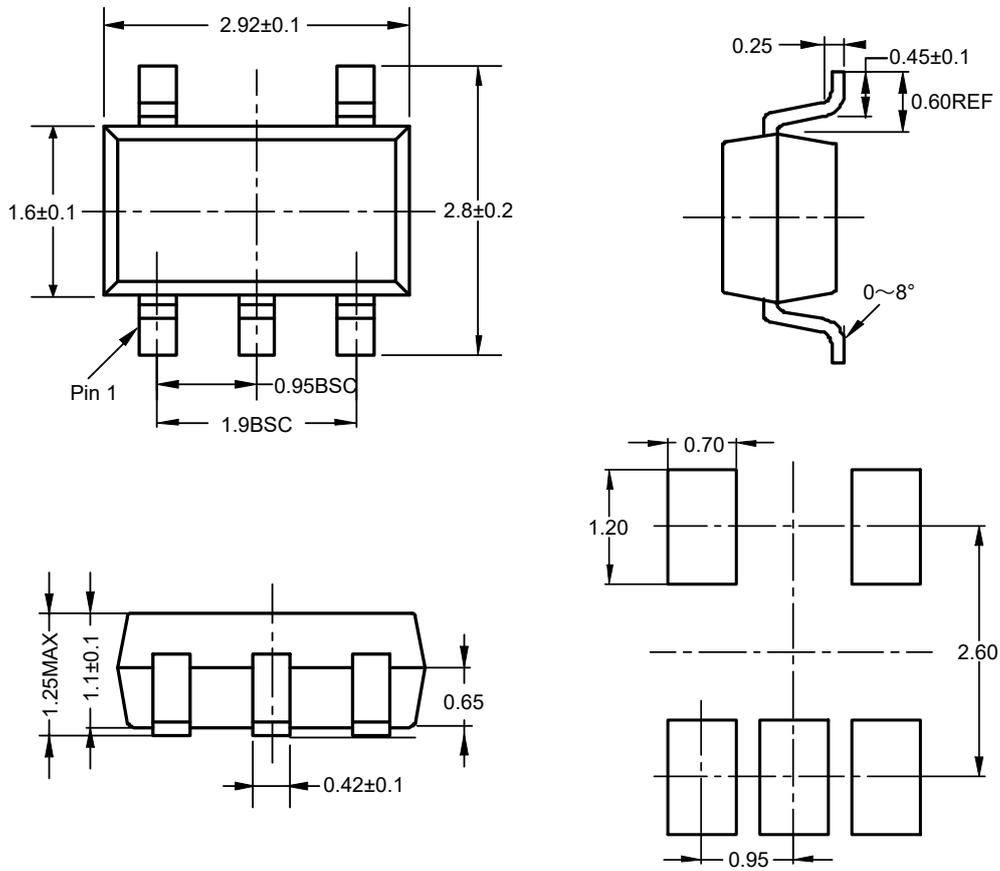


Recommended Land Pattern

Unit: mm

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SOT23-5

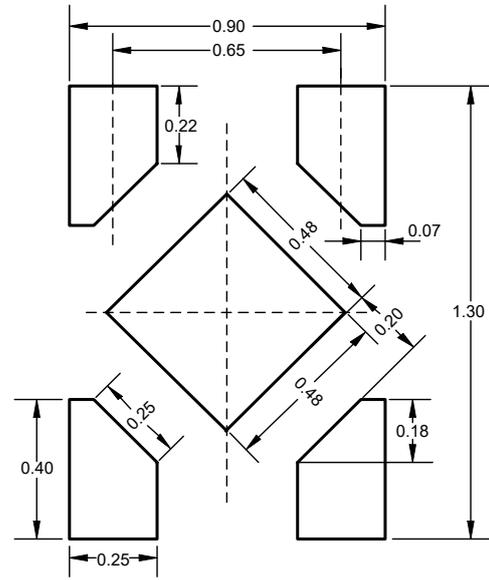
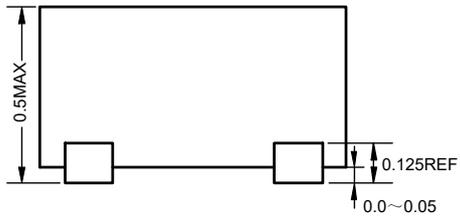
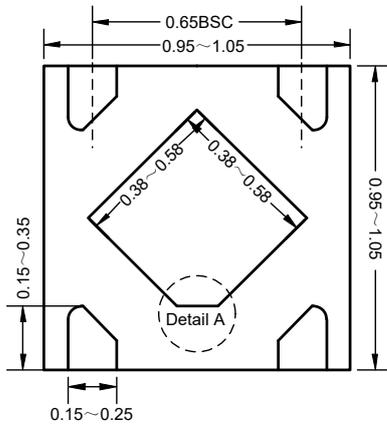


Recommended Land Pattern

Unit: mm

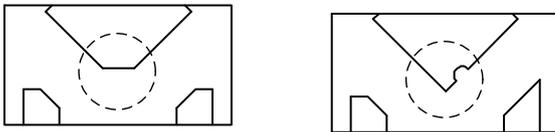
ET5H5XX

DFN4(1x1)



Recommended Land Pattern

Detail A: (PIN1 shape)

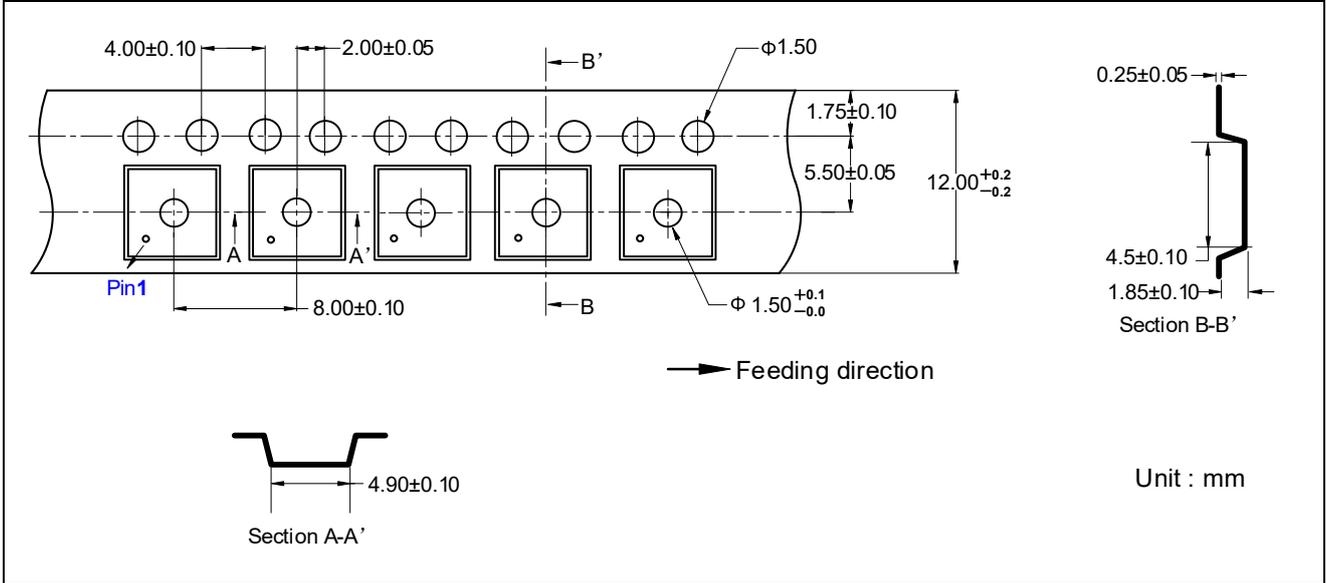


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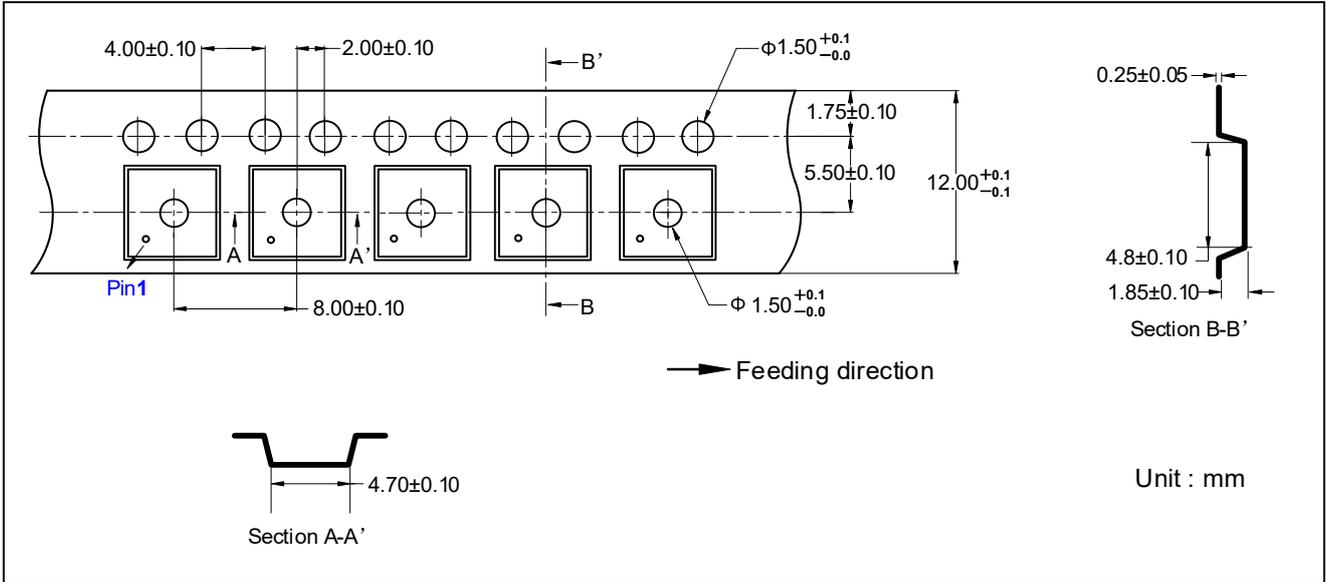
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Tape Information

SOT89-3



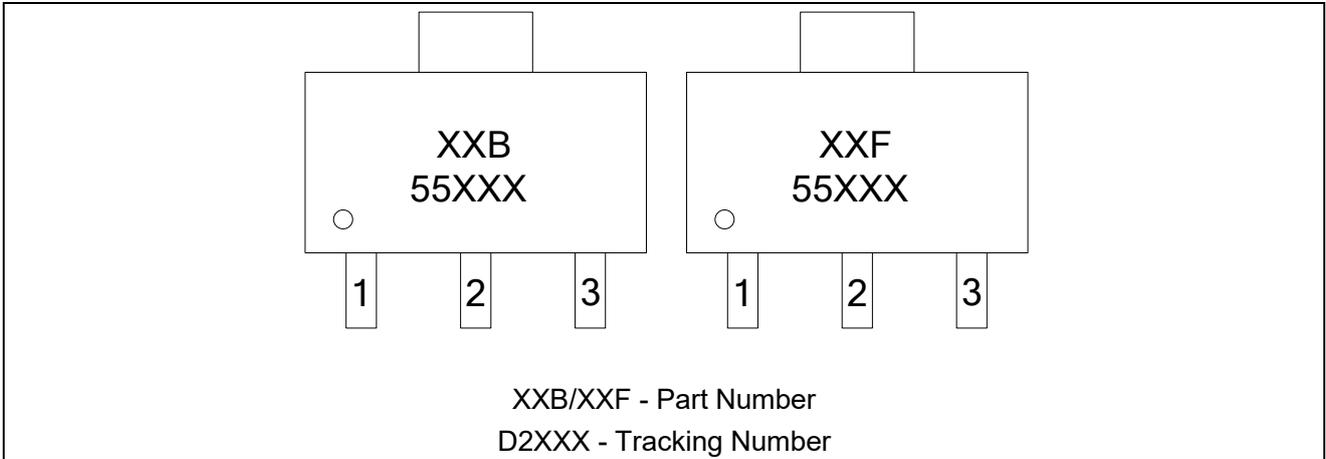
SOT89-5



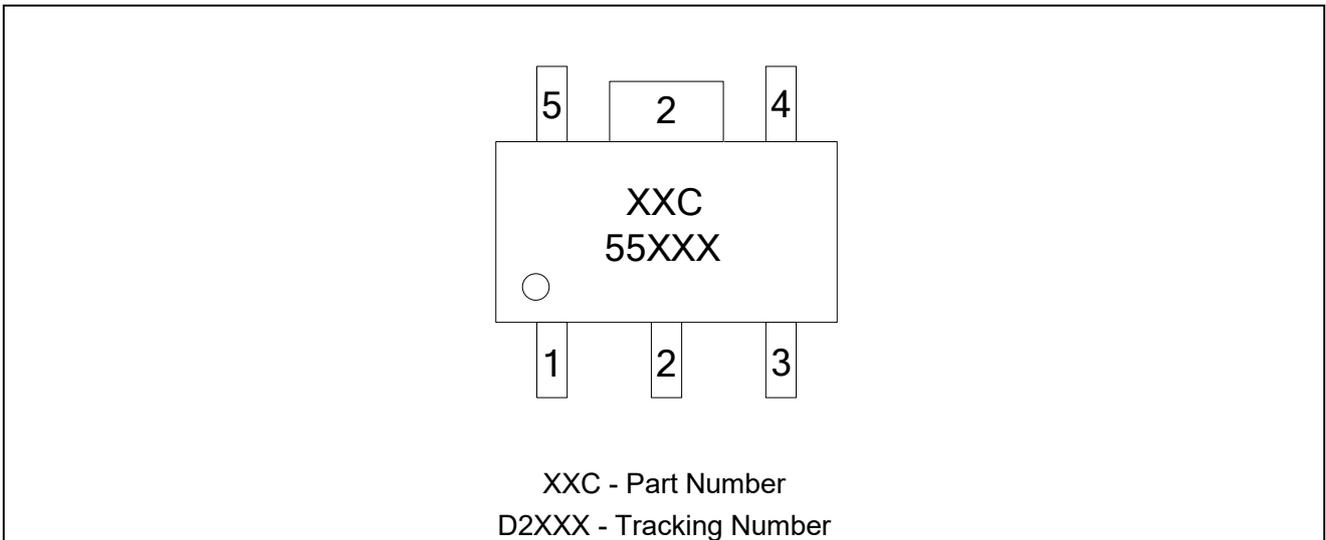
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Marking Information

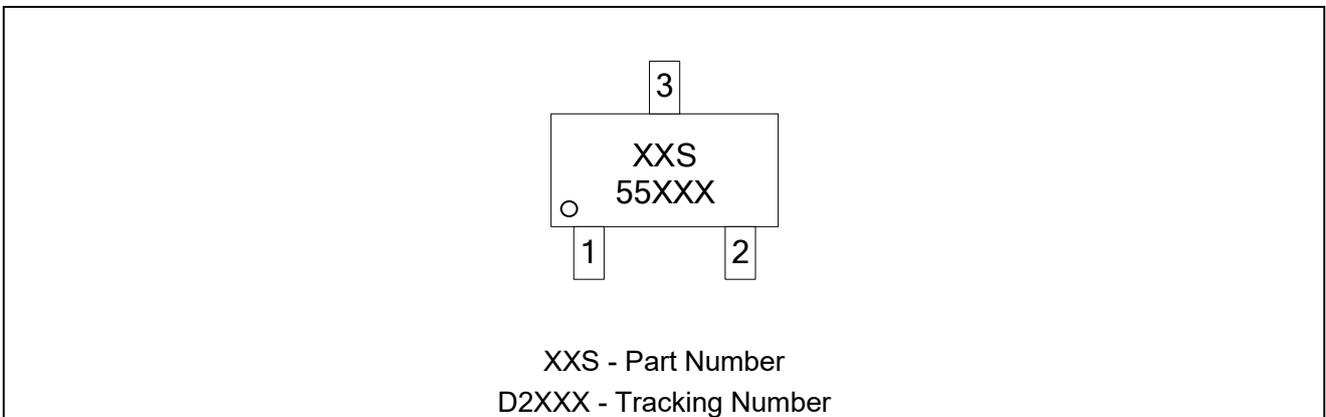
SOT89-3



SOT89-5

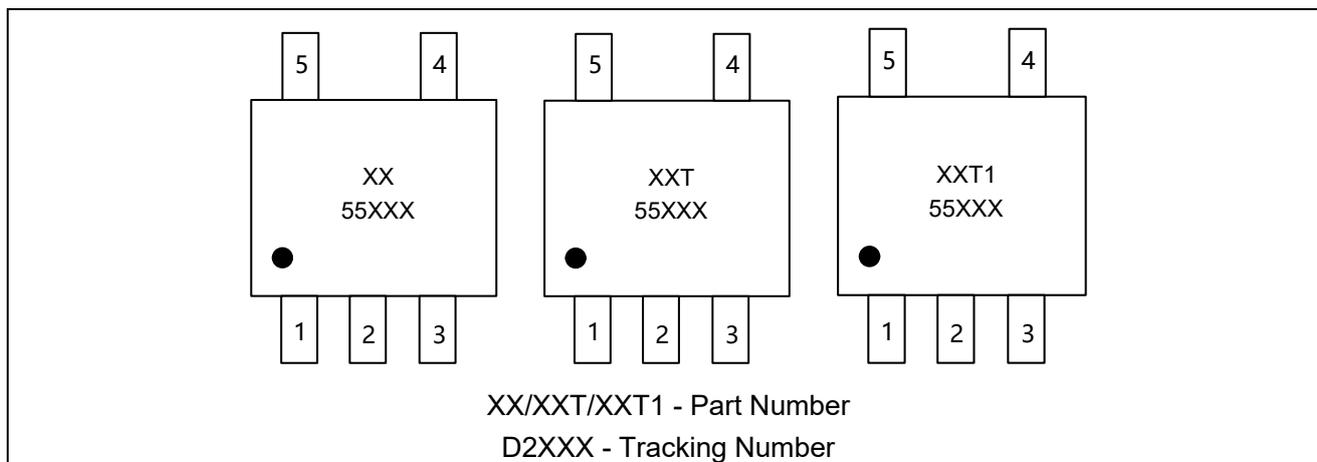


SOT23-3

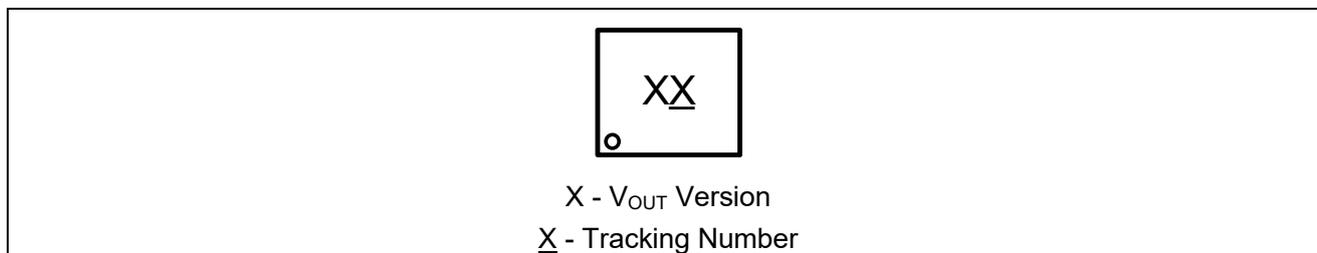


ET5H5XX

SOT23-5



DFN4



Revision History and Checking Table

Version	Date	Revision Item	Modifier	Function & Spec Checking	Package & Tape Checking
0.0	2022-11-13	Preliminary Version	Liuxm	Liuxm	Zhuji
1.0	2023-05-23	Official Version	Yangz	Tugz	Liuji
1.1	2023-10-7	Update package	Shibo	Shibo	Shibo
1.2	2024-3-10	Add 4.0V 5.3V version	Shibo	Shibo	Shibo
1.3	2025-3-19	Add XXB package	Shibo	Shibo	Liuji
1.4	2025-5-20	Update Packages	Pengjj	Liuxm	Liuji
1.5	2025-07-01	Add Packing option	Yangxiaoxu	Liuxm	Liuji
1.6	2025-08-07	Update Pin Configuration	Pengjj	Liuxm	Liuji